

EECE 351: Electronics I, Fall 2011

Class: MTHF: 10.00-10.50 a.m, BA 645

Office Hours: TBD

TA: Nikhil Bhandari, email:

Instructor: M. Cahay, Professor of Electrical and Computer Engineering and Computer Science

Prerequisite: Network Analysis I and II.

Text: Sedra and Smith, "Microelectronic Circuits, 6th Ed.", Holt, Rinehart, and Winston (2010)

Topics: Textbook Sections

- Amplifiers and Circuit Models: 1.4 - 1.6
- Operational Amplifiers: 2.1 - 2.8
- Diodes: 3.1 - 3.7
- Bipolar Junction Transistors: 5.1 - 5.7, 5.10
- Field Effect Transistors: 4.1 - 4.7, 4.11
- Current Sources, Current Mirrors and Current Steering Circuits: 6.3, 6.12
- Differential and Multistage Amplifiers: 7.1 - 7.3

Exams:

- Midterm 1 - chapters 1 and 2 (closed book)
- Midterm 2 - chapters 3 and 5 (closed book)
- Final - chapters 4,5,6,7 (closed book)

Grading:

- Two mid-terms 25% each
- Final 30%
- Quizzes 10%: will be announced in class. You will have 10 to 15 minutes at the end of the class period to solve the quiz.
- Homeworks 10%: <http://www.ece.uc.edu/~mcahay/eece351.html> or will be distributed in class

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List of ABET outcomes: a_1 , a_2 , a_3 , c, e, k.