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Education

- PhD, Computer Science, University of Louisiana at Lafayette, 1987.
- M.S., Computer Science, University of Louisiana at Lafayette, 1985.
- B.S., Mathematics, Illinois State University, 1981.

Employment History

- 2003– *Professor*, Department of Electrical and Computer Engineering, University of Cincinnati, Cincinnati, OH.
- 2000–03 *Associate Professor*, Department of Electrical & Computer Engineering and Computer Science, University of Cincinnati, Cincinnati, OH.
- 1987–00 *Assistant Professor* (tenured 9/93), Department of Electrical & Computer Engineering and Computer Science, University of Cincinnati, Cincinnati, OH.
- 1982–87 *Research Assistant*, The Center for Advanced Computer Studies, University of Southwestern Louisiana, Lafayette, LA.
- 1982 *Teaching Assistant*, Department of Computer Science, University of Southwestern Louisiana, Lafayette, LA.
- 1981–82 *Computer Programmer*, Bob White Computing and Software, Bloomington, Illinois.
- 1980–81 *Computer Programmer*, Union Insurance Group, Bloomington, Illinois.

Current Research Interests

My research is primarily in **High Performance Computing** with applications to *Data Science* and *Parallel Discrete-Event Simulation*. I also have interests in Privacy Preserving Data Mining, Embedded Systems, and Point-of-Care medical devices. I have been working with Partitioning, Parallelism, and Approximate Methods to improve the performance of *Topological Data Analysis* (TDA), specifically the computation of *Persistent Homology*. Because TDA algorithms have exponential time and space complexity, decomposing and partitioning the data into regional subspaces has dramatic impact on the overall performance. In *Big Data Clustering*, I have been working to combine random projection hashing with locality sensitive hashing to implement high-performance, distributed *privacy preserving data mining*. The projection and hashing techniques permit us to perform clustering on distributed data sets by exchanging only hash keys between the

distributed nodes. We are promoting these techniques to enable clustering and nearest neighbor search across HIPAA protected medical databases. In addition, I have been working extensively for many years to advance the field of *Parallel and Distributed Simulation* (PDES) using the Time Warp mechanism. Most recently we have been studying the design of solutions for the pending event set problem for high performance simulation on multi-core and many-core platforms. I have also initiated studies to extract profile data from discrete event simulation models to obtain quantitative data that I plan to use to focus my algorithm development for parallel simulation. Finally, I sometimes work with the local BioSensors group and the College of Medicine to develop point-of-care medical devices to assist patient monitoring, diagnosis, and treatment.

Teaching Experience

1. Graduate courses on Approximate Methods to Compute Persistent Homology High Performance Distributed Computing, Parallel Discrete Event-Driven Simulation, Parallel Processing, Computer Architecture, SIMD/MIMD Multiprocessors, Approximate Methods to Compute Persistent Homology, and Formal Semantic Models of Computer Languages.
2. Undergraduate courses on Computer System Organization, Assembler Language Programming, and Compiler Theory.

Patents

- US Patent #7,605,842, “Vehicular Optical Communications System,” October 20, 2009 (inventors: P. A. Wilsey, D. E. Martin, D. S. Dieckman, and R. F. Beyette, Jr.).
- US Patent #7,359,438, “Optical communications imager,” April 15, 2008 (inventors: R. F. Beyette, Jr., D. S. Dieckman, D. E. Martin, and P. A. Wilsey).
- US Patent #7,282,691, “Method for determining wavelengths of light incident on a photodetector,” October 16, 2007 (inventors: P. A. Wilsey, R. F. Beyette, Jr., and C. J. Fearing).
- US Patent #6,321,376, “Apparatus and method for semi-automated generation and application of language conformity tests,” November 20, 2001 (inventors: J. C. Willis, R. N. Newshutz, and P. A. Wilsey).

Publications

Book and Book Chapters

1. Nael B. Abu-Ghazaleh and Philip A. Wilsey, “Managing Control Asynchrony on SIMD Machines — A Survey,” *Advances in Computers*, Academic Press, M. Zelkowitz (ed), Volume 49, 239–302, 1999.
2. Kothanda Umamageswaran, Sheetanshu L. Pandey, and P. A. Wilsey, *Formal Semantics and Proof Techniques for Optimizing VHDL Models*, Kluwer Academic Publishers, Boston, MA, 1999. (ISBN 0–7923–8375–3).

3. Philip A. Wilsey, “Modeling, Analysis and Simulation of Computer and Telecommunication Systems,” *Encyclopedia of Computer Science and Technology*, Marcel Dekker, A. Kent and J. G. Williams (eds), Volume 41, 147–160, 1999.
4. Philip A. Wilsey, Ranga Vemuri, Peter J. Ashenden, and Norman Mause, “Programmed Monitoring and Digital System Simulation,” *Current Issues in Electronic Modeling*, Volume 8: Hardware/Software Co-Design and Co-Verification, 145–162, December 1996.
5. Philip A. Wilsey, Norman Mause, and Peter J. Ashenden, “Abstract Data Types and the Digital System Description and Simulation Environments,” *Current Issues in Electronic Modeling*, Volume 5: Hardware Component Modeling, 33–53, March 1996.
6. Philip A. Wilsey, “Developing a Formal Semantic Definition of VHDL,” in *VHDL for Simulation, Synthesis and Formal Proofs of Hardware*, J. Mermet (ed), Kluwer Academic Publishers, 245–256, 1992.

Refereed Journal

1. Rohit P. Singh, Nicholas O. Malott, and Philip A. Wilsey, “Topological Study of β -Sparsified d -Uniform Hypergraph Based Simplicial Complex,” *Journal of Big Data*, (submitted).
2. Anindya Moitra, Nicholas O. Malott, and Philip A. Wilsey, “Computation of Persistent Homology on Streaming Data using Topological Data Summaries,” *Computational Intelligence*, July 2023. doi:[10.1111/coin.12597](https://doi.org/10.1111/coin.12597)
3. Nicholas O. Malott, Shangye Chen and Philip A. Wilsey, “A Survey on the High-Performance Computation of Persistent Homology,” *IEEE Transactions on Knowledge and Data Engineering*, Volume 35, Issue 5, 4466–4484, January 2022. doi:[10.1109/TKDE.2022.3147070](https://doi.org/10.1109/TKDE.2022.3147070)
4. Kevin Jin and Philip A. Wilsey, “Guest Editorial for the TOMACS Special Issue on PADS,” *ACM Transactions on Modeling and Computer Simulation (TOMACS)*, **29**:2, March, 2019. doi:[10.1145/3312749](https://doi.org/10.1145/3312749)
5. Jonathan Lockhart, Carla Purdy, and Philip A. Wilsey, “Critical Embedded Systems Development Using Formal Methods and Statistical Reliability Metrics,” *Advances in Science, Technology and Engineering Systems Journal (ASTESJ)*, **4**:1, 231–247, 2019. doi:[10.25046/aj040123](https://doi.org/10.25046/aj040123)
6. Patrick P. Putnam, Ge Zhang, and Philip A. Wilsey, “Clotho: addressing the scalability of Forward Time Population Genetic Simulation,” *BMC Bioinformatics*, **16**:191, June 10, 2015. doi:[10.1186/s12859-015-0631-z](https://doi.org/10.1186/s12859-015-0631-z)
7. Patrick P. Putnam, Ge Zhang, and Philip A. Wilsey, “A Comparison Study of Succinct Data Structures for use in GWAS,” *BMC Bioinformatics*, **14**:369, December 21, 2013. doi:[10.1186/1471-2105-14-369](https://doi.org/10.1186/1471-2105-14-369)
8. Patrick P. Putnam, Philip A. Wilsey, and Karthik Vadambacheri Manian, “Core Frequency Adjustment to Optimize Time Warp on Many-Core Processors,” *Simulation Modelling Practice and Theory*, Volume 28, 55–64, November 2012, doi:[10.1016/j.simpat.2012.05.011](https://doi.org/10.1016/j.simpat.2012.05.011)

9. Dmitry A. Gorodetsky and Philip A. Wilsey, “Advantages of Modal Decomposition for Efficient Scaling of Parallel FDTD Computations,” *International Journal of Numerical Modeling: Electronic Networks, Devices, and Fields*, Volume 24, Number 6, 507–5124, Feb 2011. doi:[10.1002/jnm.795](https://doi.org/10.1002/jnm.795)
10. Fred R. Beyette Jr., Blane Booher, Jamey Drennan, Lee Carraher, Josh Butler, Peggy S. Bowman, Joe F. Clark, and Philip A. Wilsey, “Device for Quantification of Bilirubin in Cerebral Spinal Fluid,” *IEEE Transactions on Biomedical Engineering*, 58 (3), 773–776, March 2011. doi:[10.1109/TBME.2011.2108653](https://doi.org/10.1109/TBME.2011.2108653)
11. D. B. Pettigrew, C. J. Morgan, R. Brian Anderson, Philip A. Wilsey, and Charles Kuntz, IV, “Virtual Pre-Operative Measurement and Surgical Manipulation of Sagittal Spinal Alignment using a Novel Research and Education Software Program,” *Neurosurgical Focus*, Vol 28, No 3, 565–570, March 2010. doi:[10.3171/2009.12.FOCUS09283](https://doi.org/10.3171/2009.12.FOCUS09283)
12. Malolan Chetlur and Philip A. Wilsey, “Local Fossil Identification in Time Warp Simulations,” *Journal of Simulation and Process Modelling (IJSPM)*, Volume 5, Number 2, 95–103, September 2009. doi:[10.1504/IJSPM.2009.028623](https://doi.org/10.1504/IJSPM.2009.028623)
13. Malolan Chetlur and Philip A. Wilsey, “Causality Information and Proactive Cancellation Mechanisms,” *Concurrency and Computation: Practice and Experience*, Volume 21, Issue 11, 1483–1503, August 2009. doi:[10.1002/cpe.1399](https://doi.org/10.1002/cpe.1399)
14. Dhananjai M. Rao and Philip A. Wilsey, “Predicting Performance of Resolution Changes in Parallel Simulations,” *SCS Transactions: Simulation Journal*, Volume 84, Number 10/11, 535–555, Oct/Nov, 2008. doi:[10.1177/0037549708096134](https://doi.org/10.1177/0037549708096134)
15. Douglas R. Hickey, Chris J. Fearing, Fred R. Beyette, Jr., and Philip A. Wilsey, “Characterization of the PHOCI(TM) Data Integrated Video Sensor Technology,” *Analog Integrated Circuits and Signal Processing*, Volume 56, Issue 1–2, 61–69, August 2008. doi:[10.1007/s10470-007-9112-2](https://doi.org/10.1007/s10470-007-9112-2)
16. Dmitry A. Gorodetsky and Philip A. Wilsey, “Generation of 3-D finite-difference time-domain macromodels for faster simulation,” *International Journal of RF and Microwave Computer-Aided Engineering*, Volume 17, Number 3, 326–334, 2007. doi:[10.1002/mmce.20227](https://doi.org/10.1002/mmce.20227)
17. Dhananjai M. Rao and Philip A. Wilsey, “Accelerating ATM Simulations using Dynamic Component Substitution (DCS)” *SCS Transactions: Simulation Journal*, Volume 82, Number 4, 235–253, 2006. doi:[10.1177/0037549706067271](https://doi.org/10.1177/0037549706067271)
18. Dhananjai M. Rao and Philip A. Wilsey, “An Active Networks Simulation Environment,” *SCS Transactions: Simulation Journal*, Volume 78, Number 7, 447–460, 2002. doi:[10.1177/0037549702078007582](https://doi.org/10.1177/0037549702078007582)
19. Dhananjai M. Rao and Philip A. Wilsey, “An Ultra-large Scale Simulation Framework,” *Journal of Parallel and Distributed Computing*, Volume 62, 1670–1693, 2002. doi:[10.1016/S0743-7315\(02\)00003-5](https://doi.org/10.1016/S0743-7315(02)00003-5)
20. Dale E. Martin, Radharamanan Radhakrishnan, Dhananjai M. Rao, Malolan Chetlur, Krishnan Subramani, and Philip A. Wilsey, “Analysis and Simulation of Mixed-Technology VLSI Systems,”

- Journal of Parallel and Distributed Computing*, Volume 62, Number 3, 468–493, March 2002. doi:[10.1006/jpdc.2001.1805](https://doi.org/10.1006/jpdc.2001.1805)
21. Peter Frey, Radharamanan Radhakrishnan, Harold W. Carter, Perry Alexander, and Philip A. Wilsey, “A Formal Specification and Verification Framework for Time Warp based Parallel Simulation,” *IEEE Transactions on Software Engineering*, Volume 28, Number 1, 58–78, January 2002. doi:[10.1109/32.979989](https://doi.org/10.1109/32.979989)
 22. Vijay Balakrishnan, Radharamanan Radhakrishnan, Dhananjai M. Rao, Nael B. Abu-Ghazaleh, and Philip A. Wilsey, “A Performance and Scalability Analysis Framework for Parallel Discrete Event Simulators,” *Simulation Practice and Theory*, Volume 8, 529–553, 2001. doi:[10.1016/S0928-4869\(01\)00033-7](https://doi.org/10.1016/S0928-4869(01)00033-7)
 23. Swaminathan Subramanian, Dhananjai M. Rao, and Philip A. Wilsey, “Applying Multilevel Partitioning to Parallel Logic Simulation,” *Parallel and Distributed Computing Practices*, Volume 4, Number 1, 37–59, March 2001.
 24. Nael B. Abu-Ghazaleh and Philip A. Wilsey, “The Shared Control Parallel Architecture Model,” *Journal of Parallel and Distributed Computing*, Volume 61, Number 6, 767–783, June 2001. doi:[10.1006/jpdc.2000.1712](https://doi.org/10.1006/jpdc.2000.1712)
 25. Dhananjai M. Rao, Radharamanan Radhakrishnan, and Philip A. Wilsey, “Web-based Network Analysis and Design,” *ACM Transactions on Modeling and Computer Simulation (TOMACS)*, Volume 10, Number 1, 18–38, January 2000. doi:[10.1145/353735.353737](https://doi.org/10.1145/353735.353737)
 26. Peter J. Ashenden and Philip A. Wilsey, “Principles for Language Extensions to VHDL to Support High-Level Modeling,” *VLSI Design*. Volume 10, Number 2, 217–236, 1999. doi:[10.1155/1999/20186](https://doi.org/10.1155/1999/20186)
 27. Peter J. Ashenden and Philip A. Wilsey, “Protected Shared Variables in VHDL: IEEE Std 1076a,” *IEEE Design and Test of Computers*, Volume 16, Number 4, 74–83, October–December 1999. doi:[10.1109/54.808221](https://doi.org/10.1109/54.808221)
 28. Sheetanshu L. Pandey, Kothanda Umamageswaran, and Philip A. Wilsey, “VHDL Semantics and Validating Transformations,” *IEEE Trans. on Computer-Aided Design of Integrated Circuits and Systems*, Volume 18, Number 7, 936–955, July 1999. doi:[10.1109/43.771177](https://doi.org/10.1109/43.771177)
 29. Raghunandan Rajan, Radharamanan Radhakrishnan, and Philip A. Wilsey, “Dynamic Cancellation: Selecting Time Warp Cancellation Strategies at Runtime,” *VLSI Design*, Volume 9, Number 3, 237–251, 1999. doi:[10.1155/1999/61087](https://doi.org/10.1155/1999/61087)
 30. Nael B. Abu-Ghazaleh and Philip A. Wilsey, “Models for Control Unit Synchronization on Shared Control Architectures,” *Journal of Parallel and Distributed Computing*, Volume 52, Number 1, 69–81, July 1998. doi:[10.1006/jpdc.1998.1468](https://doi.org/10.1006/jpdc.1998.1468)
 31. Peter J. Ashenden, Philip A. Wilsey, and Dale E. Martin, “SUAVE: Extending VHDL to Improve Modeling Support,” *IEEE Design and Test of Computers*, Volume 15, Number 2, 34–44, April–June 1998. doi:[10.1109/54.679206](https://doi.org/10.1109/54.679206)

32. Kothanda Umamageswaran, Krishnan Subramani, Philip A. Wilsey, and P. Alexander, “Formal Verification and Empirical Analysis of Rollback Relaxation,” *Journal of Systems Architecture* (formerly published as *Microprocessing and Microprogramming: the Euromicro Journal*), Volume 44, Number 6, 473–495, March 1998. doi:[10.1016/S1383-7621\(97\)80167-1](https://doi.org/10.1016/S1383-7621(97)80167-1)
33. Nael B. Abu-Ghazaleh, Philip A. Wilsey, Xianzhi Fan, and Debra A. Hensgen, “Synthesizing Variable Instruction Issue Interpreters for Implementing Functional Parallelism on SIMD Computers,” *IEEE Transactions on Parallel and Distributed Systems*, Volume 8, Number 4, 412–423, April 1997. doi:[10.1109/71.588621](https://doi.org/10.1109/71.588621)
34. Nael B. Abu-Ghazaleh and Philip A. Wilsey, “Variable Instruction Scheduling for MIMD Interpretation on Pipelined SIMD Machines and for Compositional Instruction Sets,” *Concurrency—Practice and Experience*, Volume 9, Number 1, 21–39, January 1997. doi:[10.1002/\(SICI\)1096-9128\(199701\)9:1<21::AID-CPE237>3.0.CO;2-L](https://doi.org/10.1002/(SICI)1096-9128(199701)9:1<21::AID-CPE237>3.0.CO;2-L)
35. Avinash C. Palaniswamy and Philip A. Wilsey, “Parameterized Time Warp (PTW): An Integrated Adaptive Solution to Optimistic PDES,” *Journal of Parallel and Distributed Computing*, Volume 37, Number 2, 134–145, September 1996. doi:[10.1006/jpdc.1996.0114](https://doi.org/10.1006/jpdc.1996.0114)
36. Christopher H. Young and Philip A. Wilsey, “A Distributed Method to Bound Rollback Lengths for Fossil Collection in Time Warp Simulators,” *Information Processing Letters*, Volume 59, Number 4, 191–196, August 1996. doi:[10.1016/0020-0190\(96\)00107-X](https://doi.org/10.1016/0020-0190(96)00107-X)
37. Xianzhi Fan, Nael B. Abu-Ghazaleh, and Philip A. Wilsey, “On the Complexity of Scheduling MIMD Operations for SIMD Interpretation,” *Journal of Parallel and Distributed Computing*, Volume 29, Number 1, 91–95, August 1995. doi:[10.1006/jpdc.1995.1109](https://doi.org/10.1006/jpdc.1995.1109)
38. Philip A. Wilsey and Subrata Dasgupta, “A Formal Model of Computer Architectures for Digital System Design Environments,” *IEEE Trans. on Computer-Aided Design of Integrated Circuits and Systems*, Volume 9, Number 5, 473–486, May 1990. doi:[10.1109/43.55179](https://doi.org/10.1109/43.55179)
39. Subrata Dasgupta, Philip A. Wilsey, and Juha Heinanen, “Axiomatic Specifications in Firmware Development Systems,” *IEEE Software*, Volume 3, Number 4, 49–58, July 1986. doi:[10.1109/MS.1986.234068](https://doi.org/10.1109/MS.1986.234068)

Refereed Conference

1. Anurag Yadav, Rohit P. Singh, and Philip A. Wilsey, “Higher Dimensional Topological Study Using Resource Efficient Delaunay Triangulation,” *40th IEEE International Conference on Data Engineering (ICDE 2024)*, April 2024. (submitted).
2. Rohit P. Singh, Nicholas O. Malott, Blake Sauerwein, Neil McGrogan, and Philip A. Wilsey, “Tools-Generating High Dimensional Test Data for Topological Data Analysis,” *IEEE International Symposium on Workload Characterization*, October 2023. (submitted).

3. Rohit P. Singh and Philip A. Wilsey, “Persistence Homology of Proximity Hyper-Graphs for Higher Dimensional Big Data,” *IEEE International Conference on Big Data (BigData ’22)*, December 2022. doi:[10.1109/BigData55660.2022.10020926](https://doi.org/10.1109/BigData55660.2022.10020926)
4. Rohit P. Singh and Philip A. Wilsey, “Polytopal Complex Construction and Use in Persistent Homology,” *ICDM Workshop on High Dimensional Data Mining (HDM ’22)*, November 2022. doi:[10.1109/ICDMW58026.2022.00087](https://doi.org/10.1109/ICDMW58026.2022.00087)
5. Nicholas O. Malott, Robert R. Lewis, and Philip A. Wilsey, “Homology-Separating Triangulated Euler Characteristic Curve,” *IEEE International Conference on Data Mining (ICDM ’22)*, November 2022. doi:[10.1109/ICDM54844.2022.00136](https://doi.org/10.1109/ICDM54844.2022.00136)
6. Rishi R. Verma, Nicholas O. Malott, and Philip A. Wilsey, “Data Reduction and Feature Isolation for Computing Persistent Homology on High Dimensional Data,” *Workshop on Applications of Topological Data Analysis to “Big Data”*, held in conjunction with the 2021 IEEE International Conference on Big Data, 3860–3864, December 2021. doi:[10.1109/BigData52589.2021.9671839](https://doi.org/10.1109/BigData52589.2021.9671839)
7. Nicholas O. Malott, Rishi R. Verma, Rohit P. Singh, and Philip A. Wilsey, “Distributed Computation of Persistent Homology from Partitioned Big Data,” *IEEE International Conference on Cluster Computing (Cluster 2021)*, 344–354, September 2021. doi:[10.1109/Cluster48925.2021.00050](https://doi.org/10.1109/Cluster48925.2021.00050)
8. Nick O. Malott, Aaron M. Sens, and Philip A. Wilsey, “Topology Preserving Data Reduction for Computing Persistent Homology,” *International Workshop on Big Data Reduction (IWBDR 20)*, held in conjunction with the 2020 IEEE International Conference on Big Data, 2681–2690, December 2020. doi:[10.1109/BigData50022.2020.9378216](https://doi.org/10.1109/BigData50022.2020.9378216)
9. Anindya Moitra, Nicholas O. Malott, and Philip A. Wilsey, “Persistent Homology on Streaming Data,” *8th Workshop on Data Mining in Biomedical Informatics and Healthcare (DMBIH’20)*, held in conjunction with the IEEE International Conference on Data Mining, 636–643, November 2020. doi:[10.1109/ICDMW51313.2020.00090](https://doi.org/10.1109/ICDMW51313.2020.00090)
10. Nick O. Malott and Philip A. Wilsey, “Fast Computation of Persistent Homology with Data Reduction and Data Partitioning,” *IEEE International Conference on Big Data (BigData 19)*, pp 880–889, December 2019. doi:[10.1109/BigData47090.2019.9006572](https://doi.org/10.1109/BigData47090.2019.9006572)
11. Philip A. Wilsey, “Time Warp Simulation on Multi-Core Platforms”, *Proceedings of the 2019 Winter Simulation Conference*, pp. 1454–1468, December 2019, (invited). doi:[10.1109/WSC40007.2019.9004880](https://doi.org/10.1109/WSC40007.2019.9004880)
12. Sean Kane, Sounak Gupta, and Philip A. Wilsey, “Analyzing Simulation Model Profile Data to Assist Synthetic Model Generation,” *The 23rd International Symposium on Distributed Simulation and Real Time Applications (DS-RT 2019)*, pp 1–10, October 2019. doi:[10.1109/DS-RT47707.2019.8958699](https://doi.org/10.1109/DS-RT47707.2019.8958699)
13. Sayantan Dey, Lee A. Carraher, Anindya Moitra, and Philip A. Wilsey, “Clustering Data in Secured, Distributed Datasets,” *The Third International Workshop on Parallel and Distributed Data Mining (WPDM 2019)* part of *Computational Science and Its Applications (ICCSA 2019)*, 557–572, July 2019. doi:[10.1007/978-3-030-24311-1_40](https://doi.org/10.1007/978-3-030-24311-1_40)

14. Lee A. Carraher, Sayantan Dey, and Philip A. Wilsey, “Tree Based Clustering On Large, High Dimensional Datasets,” *MLDM 2019: 15th International Conference on Machine Learning and Data Mining*, July 2019.
15. Anindya Moitra, Nick Malott, and Philip A. Wilsey, “Cluster-based Data Reduction for Persistent Homology,” *2018 IEEE International Conference on Big Data (BigData 18)*, 327–334, December 2018. doi:[10.1109/BigData.2018.8622440](https://doi.org/10.1109/BigData.2018.8622440)
16. Patrick Crawford, Peter D. Barnes Jr., Stephan J. Eidenbenz, and Philip A. Wilsey, “Sampling Simulation Model Profile Data for Analysis,” *ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS 2018)*, May 2018, doi:[10.1145/3200921.3200944](https://doi.org/10.1145/3200921.3200944)
17. Patrick Crawford, Stephan J. Eidenbenz, Peter D. Barnes Jr., and Philip A. Wilsey, “Some Properties of Communication Behaviors in Discrete-Event Simulation Models,” *Proceedings of the 2017 Winter Simulation Conference*, December 2017. (invited). doi:[10.1109/WSC.2017.8247852](https://doi.org/10.1109/WSC.2017.8247852)
18. Sounak Gupta and Philip A. Wilsey, “Quantitative Driven Optimization of a Time Warp Kernel,” *ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS 2017)*, May 2017. doi:[10.1145/3064911.3064932](https://doi.org/10.1145/3064911.3064932).
19. Barry Williams, Dmitry Ponomarev, Nael Abu-Ghazaleh, and Philip A. Wilsey, “Performance Characterization of Parallel Discrete Event Simulation on Knights Landing Processor,” *ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS 2017)*, May 2017. doi:[10.1145/3064911.3064929](https://doi.org/10.1145/3064911.3064929)
20. Lee A. Carraher, Philip A. Wilsey, Anindya Moitra, and Sayantan Dey, “Random Projection Clustering on Streaming Data,” *IEEE ICDM Workshop on High Dimensional Data Mining (HDM ’16)*, December 2016. doi:[10.1109/ICDMW.2016.0105](https://doi.org/10.1109/ICDMW.2016.0105).
21. Jacob Franklin, Samuel Wenke, Sadiq Quasem, Lee A. Carraher, and Philip A. Wilsey, “streamingRPHash: Random Projection Clustering of High-Dimensional Data in a MapReduce Framework,” *IEEE Cluster 2016*, September 2016. doi:[10.1109/CLUSTER.2016.89](https://doi.org/10.1109/CLUSTER.2016.89)
22. Philip A. Wilsey, “Some Properties of Events Executed in Discrete-Event Simulation Models,” *ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS 2016)*, 165–176, May 2016. doi:[10.1145/2901378.2901400](https://doi.org/10.1145/2901378.2901400)
23. Lee A. Carraher, Philip A. Wilsey, Anindya Moitra, and Sayantan Dey, “Multi-probe random projection clustering to secure very large distributed datasets”, *2015 IEEE International Conference on Big Data (Big Data)*, 1891–1900 Oct, 2015. doi:[10.1109/BigData.2015.7363964](https://doi.org/10.1109/BigData.2015.7363964)
24. Joshua Hay and Philip A. Wilsey, “Experiments with Hardware-based Transactional Memory in Parallel Simulation,” *ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS 2015)*, June 2015. doi:[10.1145/2769458.2769462](https://doi.org/10.1145/2769458.2769462)
25. A. J. Alt and Philip A. Wilsey, “Profile Driven Partitioning of Parallel Simulation Models,” *Proceedings of the 2014 Winter Simulation Conference*, December 2014. doi:[10.1109/WSC.2014.7020118](https://doi.org/10.1109/WSC.2014.7020118)

26. William A. Magato and Philip A. Wilsey, “llamaOS: A Solution for Virtualized High Performance Computing Clusters,” *Workshop on Parallel and Distributed Scientific and Engineering Computing*, May 2014. doi:[10.1109/IPDPSW.2014.129](https://doi.org/10.1109/IPDPSW.2014.129).
27. Sounak Gupta and Philip A. Wilsey, “Lock-Free Pending Event Set Management in Time Warp,” *ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS 2014)*, May 2014. doi:[10.1145/2601381.2601393](https://doi.org/10.1145/2601381.2601393)
28. Vivnesh Subbian, Philip A. Wilsey, and Fred R. Beyette Jr. “Heuristic evaluation of user interface for point-of-care diagnosis and rehabilitation of mild Traumatic Brain Injury,” *Conference on Neural Engineering (NER), 2013 6th International IEEE/EMBS*, pp. 1250–1253, IEEE, 2013. doi:[10.1109/NER.2013.6696167](https://doi.org/10.1109/NER.2013.6696167)
29. Vignesh Subbian, Fred R. Beyette Jr., and Philip A. Wilsey, “Development and Evaluation of Hardware for Point-of-Care Assessment of Upper-limb Motor Performance,” *35th Annual International Conference of the IEEE Engineering in Medicine and Biology Society*, July 2013. doi:[10.1109/EMBC.2013.6609656](https://doi.org/10.1109/EMBC.2013.6609656)
30. Tom Dickman, Sounak Gupta, and Philip A. Wilsey, “Event Pool Structures for PDES on Many-Core Beowulf Clusters,” *ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS 2013)*, May 2013. doi:[10.1145/2486092.2486106](https://doi.org/10.1145/2486092.2486106)
31. Lee Carraher, Philip A. Wilsey, and Fred S. Annexstein, “A GPGPU Algorithm for c-Approximate r-Nearest Neighbor Search in High Dimensions,” *International Workshop on High Performance Data Intensive Computing (HPDIC 2013)*, May 2013, doi:[10.1109/IPDPSW.2013.239](https://doi.org/10.1109/IPDPSW.2013.239)
32. Ryan Child and Philip A. Wilsey, “Using DVFS to Optimize Time Warp Simulations,” *Proceedings of the 2012 Winter Simulation Conference*, December 2012. doi:[10.1109/WSC.2012.6465146](https://doi.org/10.1109/WSC.2012.6465146)
33. Vignesh Subbian, Fred R. Beyette Jr., and Philip A. Wilsey, “Design and Usability of a Medical Computing System for Diagnosis of Mild Traumatic Brain Injury,” *34th Annual International Conference of the IEEE Engineering in Medicine and Biology Society*, Sept 2012. doi:[10.1109/EMBC.2012.6346101](https://doi.org/10.1109/EMBC.2012.6346101)
34. Ryan Child and Philip A. Wilsey, “Dynamically Adjusting Core Frequencies to Accelerate Time Warp Simulations in Many-Core Processors,” *26th Workshop on Principles of Advanced and Distributed Simulation (PADS 2012)*, July 2012, doi:[10.1109/PADS.2012.15](https://doi.org/10.1109/PADS.2012.15)
35. Karthik Vadambacheri Manian and Philip A. Wilsey, “Distributed Simulation on a Many-Core Processor,” *The Third International Conference on Advances in System Simulation (SIMUL 2011)*, Oct 2011.
36. Edward C. Herrmann, Prudhvi Janga, and Philip A. Wilsey, “Pre-computing Function Results in Multi-Core and Many-Core Processors,” *The Second International Workshop on Parallel Software Tools and Tool Infrastructures (PSTI 2011)*, Sept 2011. doi:[10.1109/ICPPW.2011.46](https://doi.org/10.1109/ICPPW.2011.46)
37. Fred R. Beyette Jr., Blaine Booher, James Drennan, Lee Carraher, Josh Butler, Peggy Bowman, Joseph F. Clark, and Philip A. Wilsey, “Development of a Point-Of-Care Device for the Quantification of

- Bilirubin in Cerebral Spinal Fluid,” *32nd Annual International Conference of the IEEE Engineering in Medicine and Biology Society*, 915-8, Sept 2010, doi:[10.1109/IEMBS.2010.5627481](https://doi.org/10.1109/IEMBS.2010.5627481)
38. Chandra K. Kuchi and Philip A. Wilsey, “Multi-View and Pause-View in a Second Life Client,” *2010 International Symposium on Collaborative Technologies and Systems (CTS 2010)*, May 2010. doi:[10.1109/CTS.2010.5478515](https://doi.org/10.1109/CTS.2010.5478515)
 39. M-J Kim, J-Y Lee, H-Y Chang, S-J Cho, Y. Park, and Philip A. Wilsey, “Design and Performance Evaluation of Binary Code Packing for Protecting Embedded Software against Reverse Engineering,” *Proceedings of the 13th IEEE International Symposium on Object/component/service-oriented Real-time distributed computing*, May 2010. doi:[10.1109/ISORC.2010.23](https://doi.org/10.1109/ISORC.2010.23)
 40. Edward C. Herrmann and Philip A. Wilsey, “Threaded Dynamic Memory Management in Many-Core Processors,” *The Fourth International Conference on Complex, Intelligent and Software Intensive Systems (CISIS 2010)*, 931–936, February 2010, doi:[10.1109/CISIS.2010.34](https://doi.org/10.1109/CISIS.2010.34)
 41. R. Brian Anderson, Mike Borowczak, and Philip A. Wilsey, “The Use of Device Simulation in Development of USB Storage Devices,” *Proc. of the 41th Annual Simulation Symposium*, 220-226, April 2008.
 42. Malolan Chetlur and Philip A. Wilsey, “Working Set Based Scheduling in Time Warp Simulations,” *Proc. of the 40th Annual Simulation Symposium*, 221–230, April 2007.
 43. Malolan Chetlur and Philip A. Wilsey, “Causality Information and Fossil Collection in Time Warp Simulations,” *Proceedings of the 2006 Winter Simulation Conference*, 987–994, December 2006.
 44. Dhananjai M. Rao and Philip A. Wilsey, “Applying Parallel, Dynamic-Resolution Simulations to Accelerate VLSI Power Estimation,” *Proceedings of the 2006 Winter Simulation Conference*, 694–702, December 2006.
 45. Malolan Chetlur and Philip A. Wilsey, “Causality and Proactive Cancellation,” *10th IEEE/ACM International Symposium on Distributed Simulation (DS-RT 2006)*, 193–200, October 2006.
 46. Dhananjai M. Rao and Philip A. Wilsey, “Predicting Performance of Resolution Changes in Parallel Simulations,” *20th Workshop on Principles of Advanced and Distributed Simulation (PADS 2006)*, 45–54, May 2006.
 47. Douglas R. Hickey, Philip A. Wilsey, Robert J. Hoekstra, Eric R. Keiter, Scott A. Hutchinson, and Thomas V. Russo, “Mixed-Signal Simulation with the Simbus Backplane,” *Proc. of the 39th Annual Simulation Symposium*, 223–229 April 2006.
 48. Dmitry A. Gorodetsky and Philip A. Wilsey, “Reducing the setup time of a one-step FDTD method,” *International Conference on Parallel and Distributed Processing Techniques and Applications*, 431–436, 2006.
 49. Dmitry A. Gorodetsky and Philip A. Wilsey, “Interfacing multiple macromodels constructed with the FDTD method,” *IEEE Northeast Workshop on Circuits and Systems*, 293–296, June 2006.

50. Dmitry A. Gorodetsky and Philip A. Wilsey, "Rapid evaluation of macromodel response with the FDTD method," *International Conference on Scientific Computing*, 267–271, 2006.
51. Dmitry A. Gorodetsky and Philip A. Wilsey, "Innovative approaches to parallelizing finite-difference time-domain computations," *IEEE Workshop on Direct and Inverse Problems in Electrodynamics*, 43–47, Sept 2005.
52. Dhananjai M. Rao and Philip A. Wilsey, "Accelerating Spatially Explicit Simulations of Spread of Lyme Disease," *Proc. of the 38th Annual Simulation Symposium*, 251–258, April 2005.
53. Dale E. Martin, Philip A. Wilsey, Robert J. Hoekstra, Eric R. Keiter, Scott A. Hutchinson, Thomas V. Russo, and Lon J. Waters, "Scheduling Optimization on the Simbus Backplane," *Proc. of the 37th Annual Simulation Symposium*, 231–237, April 2004.
54. Dale E. Martin, Philip A. Wilsey, Robert J. Hoekstra, Eric R. Keiter, Scott A. Hutchinson, Thomas V. Russo, and Lon J. Waters, "Redesigning the WARPED Simulation Kernel for Analysis and Application Development," *Proc. of the 36th Annual Simulation Symposium*, 216–223, April 2003.
55. Dhananjai M. Rao and Philip A. Wilsey, "Performance Prediction of Dynamic Component Substitutions," *Proceedings of the 2002 Winter Simulation Conference*, 816–824, December 2002.
56. Radharamanan Radhakrishnan and Philip A. Wilsey, "Software Control Systems for Parallel Simulation," *16th Workshop on Parallel and Distributed Simulation (PADS 2002)*, 135–142, May 2002.
57. Dale E. Martin, Philip A. Wilsey, Robert J. Hoekstra, Eric R. Keiter, Scott A. Hutchinson, and Lon J. Waters, "Integrating Multiple Parallel Simulation Engines for Mixed-Technology Parallel Simulation," *Proc. of the 35th Annual Simulation Symposium*, 45–52, April 2002.
58. Nael B. Abu-Ghazaleh and Philip A. Wilsey, "On the Organization of Concurrent Interpreters," *Second Workshop on Massively Parallel Processing (WMPP '02)*, April 2002.
59. K. K. Rangan, Nael B. Abu-Ghazaleh, and Philip A. Wilsey, "A Distributed Multiple-SIMD Intelligent Memory," *2001 International Conference on Parallel Processing (ICPP)*, 507–516, September 2001.
60. J. Dahl, Malolan Chetlur, and Philip A. Wilsey, "Event List Management In Distributed Simulation," *European Parallel Computing Conference (Euro-Par '01)*, 466–475, August 2001.
61. Dhananjai M. Rao and Philip A. Wilsey, "Multi-Resolution Network Simulations Using Dynamic Component Substitution," *Proceedings of the Ninth International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS 2001)*, August 2001.
62. Malolan Chetlur and Philip A. Wilsey, "Causality Representation and Cancellation Mechanism in Time Warp Simulations," *15th Workshop on Parallel and Distributed Simulation (PADS 2001)*, 165–172, May 2001.
63. Krishna K. Rangan, Nilesh Pisolkar, Nael B. Abu-Ghazaleh, and Philip A. Wilsey, "PPIM-SIM: An Efficient Simulator for a Parallel Processor in Memory," *Proc. of the 34th Annual Simulation Symposium*, 117–124, April 2001.

64. Dhananjai M. Rao and Philip A. Wilsey, "Modeling and Simulation of Active Networks," *Proc. of the 34th Annual Simulation Symposium*, 177–184, April 2001.
65. Krishna K. Rangan, Nilesh Pisolkar, Nael B. Abu-Ghazaleh, and Philip A. Wilsey, "Architectural Support for Data-Intensive Applications," *Workshop on Massively Parallel Processing (WMPP '01)*, April 2001.
66. Dhananjai M. Rao, Harold W. Carter, and Philip A. Wilsey, "Optimizing Costs of Web-based Modeling and Simulation," *International Workshop on Internet Computing and E-Commerce (ICEC'01)*, April 2001.
67. Dhananjai M. Rao and Philip A. Wilsey, "Dynamic Component Substitution in Web-Based Simulation," *Proceedings of the 2000 Winter Simulation Conference*, 1840–1848, December 2000.
68. Dhananjai M. Rao and Philip A. Wilsey, "Parallel Co-simulation of Conventional and Active Networks," *Proceedings of the Eighth International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS 2000)*, 291–298, August 2000.
69. P. Frey, Radharamanan Radhakrishnan, Harold W. Carter, and Philip A. Wilsey, "Parallel Mixed-Technology Simulation," *14th Workshop on Parallel and Distributed Simulation (PADS 2000)*, 7–14, May 2000.
70. Swaminathan Subramanian, Dhananjai M. Rao, and Philip A. Wilsey, "Study of a Multilevel Approach to Partitioning for Parallel Logic Simulation," *International Parallel and Distributed Processing Symposium (IPDPS 2000)*, 833–838, May 2000.
71. Dhananjai M. Rao, V. Chernyakhovsky, and Philip A. Wilsey, "WESE: A Web-based Environment for Systems Engineering," *2000 International Conference On Web-Based Modeling & Simulation (WEBSIM 2000)*, January 2000.
72. Philip A. Wilsey, "Web-Based Analysis and Distributed IP," *Proceedings of the 1999 Winter Simulation Conference*, P. A. Farrington, H. B. Nembhard, D. T. Sturrock, and G. W. Evans (eds), 1445–1453, December 1999.
73. Dhananjai M. Rao, and Philip A. Wilsey, "An Object Oriented Framework for Parallel Simulation of Ultra-large Communication Networks," *The Third International Symposium on Computing in Object-Oriented Parallel Environments (ISCOPE '99)*, (LNCS 1732), S. Matsuoka, R. R. Oldehoeft, and M. Tholburn (eds), 37–48, December 1999.
74. Radharamanan Radhakrishnan and Philip A. Wilsey, "Ruminations on the Implications of Multi-Resolution Modeling on DIS/HLA," *Third International Workshop on Distributed Interactive Simulation and Real Time Applications (DIS-RT '99)*, October 1999.
75. Dhananjai M. Rao and Philip A. Wilsey, "Simulation of Ultra-large Communication Networks," *Proceedings of the Seventh International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS '99)*, 112–119, October 1999.

76. Girindra D. Sharma, Nael B. Abu-Ghazaleh, Umesh Kumar V. Rajasekaran, and Philip A. Wilsey, “Optimizing Message Delivery in Asynchronous Distributed Applications,” *European Parallel Computing Conference (Euro-Par ’99)*, (LNCS 1685), P Amestoy *et al* (eds), 1204–1208, September 1999.
77. Girindra D. Sharma, Radharamanan Radhakrishnan, Umesh Kumar V. Rajasekaran, Nael B. Abu-Ghazaleh, and Philip A. Wilsey, “Time Warp Simulation on Clumps,” *13th Workshop on Parallel and Distributed Simulation (PADS ’99)*, 174–181, May 1999.
78. Christopher H. Young, Radharamanan Radhakrishnan, and Philip A. Wilsey, “Optimism: Not Just for Event Execution Anymore,” *13th Workshop on Parallel and Distributed Simulation (PADS ’99)*, 136–143, May 1999.
79. Victoria Chernyakhovsky, Peter Frey, Radharamanan Radhakrishnan, Philip A. Wilsey, Perry Alexander, and Harold W. Carter, “A Formal Framework for Specifying and Verifying Time Warp Optimizations,” *Workshop on Formal Methods for Parallel Programming: Theory and Applications*, (LNCS 1586), J. Rolim *et al* (eds), 1228–1242, April 1999. doi:[10.1007/BFb0098005](https://doi.org/10.1007/BFb0098005)
80. Umesh Kumar V. Rajasekaran, Malolan Chetlur, Girindra D. Sharma, Radharamanan Radhakrishnan, and Philip A. Wilsey, “Addressing Communication Latency Issues on Clusters for Fine Grained Asynchronous Applications — A Case Study,” *International Workshop on Personal Computer based Networks Of Workstations (PC-NOW ’99)*, (LNCS 1586), J. Rolim *et al* (eds), 1145–1162, April 1999.
81. Narayanan V. Thondugulam, Dhananjai M. Rao, and Philip A. Wilsey, “Relaxing Causal Constraints in PDES,” *13th International Parallel Processing Symposium (IPPS/SPDP ’99)*, 696–700, April 1999.
82. Peter J. Ashenden, Robert Esser, and Philip A. Wilsey, “Communication and Synchronization Using Bounded Channels in SUAVE,” *Proceedings of International Hardware Description Languages Conference (HDLCON ’99)*, 111–118, April 1999.
83. Dhananjai M. Rao, Radharamanan Radhakrishnan, and Philip A. Wilsey, “FWNS: Framework for Web-based Network Simulation,” *1999 International Conference On Web-Based Modeling & Simulation (WEBSIM 99)*, A. G. Bruzzone, A. Uhrmacher, E. H. Page (eds), Volume 31, Number 3, 9–14, January 1999.
84. Christopher H. Young, Nael B. Abu-Ghazaleh, Radharamanan Radhakrishnan, and Philip A. Wilsey, “Performance Benefits of Optimism in Fossil Collection,” *32th Hawaii International Conference on System Sciences (HICSS-32)*, January 1999.
85. Peter Frey, Radharamanan Radhakrishnan, Philip A. Wilsey, Perry Alexander, and Harold W. Carter, “An Extensible Formal Framework for the Specification and Verification of an Optimistic Simulation Protocol,” *32th Hawaii International Conference on System Sciences (HICSS-32)*, January 1999.
86. Radharamanan Radhakrishnan, Dale E. Martin, Malolan Chetlur, Dhananjai M. Rao, and Philip A. Wilsey, “An Object-Oriented Time Warp Simulation Kernel,” *The Second International Symposium on Computing in Object-Oriented Parallel Environments (ISCOPE ’98)*, (LNCS 1505), D. Caromel, R. R. Oldehoeft, and M. Tholburn (eds), 13–23, December 1998.

87. Dhananjai M. Rao, Narayanan V. Thondugulam, Radharamanan Radhakrishnan, and Philip A. Wilsey, "Unsynchronized Parallel Discrete Event Simulation," *Proceedings of the 1998 Winter Simulation Conference*, D. J. Medeiros, E. F. Watson, J. S. Carson, and M. S. Manivannan (eds), 1563–1570, December 1998.
88. Dhananjai M. Rao, K. Swaminathan, Radharamanan Radhakrishnan, Philip A. Wilsey, and Perry Alexander, "ANSE: An Active Networks Simulation Environment," *Workshop on Distributed and Parallel Systems (DAPSYS 98)*, 127–131, September 1998.
89. Christopher H. Young, Nael B. Abu-Ghazaleh, and Philip A. Wilsey, "OFC: A Distributed Fossil-Collection Algorithm for Time-Warp," *12th International Symposium on Distributed Computing (DISC 98)*, (LNCS 1499), S. Kutten (ed), 408–418, September 1998.
90. Malolan Chetlur, Girindra D. Sharma, N. Abu-Ghazaleh, U. Rajasekaran, and Philip A. Wilsey, "An Active Layer Extension to MPI," *EuroPVM/MPI '98*, (LNCS 1497), V. Alexandrov and J. Dongarra (eds), 97–104, September 1998.
91. Peter J. Ashenden and Philip A. Wilsey and Dale E. Martin, "SUAVE: Object-Oriented and Genericity Extensions to VHDL for High-Level Modeling," *Proceedings of Forum on Design Languages (FDL98)*, 109–118, September 1998.
92. Nael B. Abu-Ghazaleh and Philip A. Wilsey, "Shared Control — Supporting Control Parallelism using SIMD-like Architectures," *European Parallel Computing Conference (Euro-Par '98)*, (LNCS 1470), D. Pritchard and J. Reeve (eds), 1089–1099, September 1998.
93. Radharamanan Radhakrishnan, N. Abu-Ghazaleh, Malolan Chetlur, and Philip A. Wilsey, "On-line Configuration of a Time Warp Parallel Discrete Event Simulator," *1998 International Conference on Parallel Processing*, 28–35, August 1998.
94. Peter J. Ashenden and Philip A. Wilsey, "Extensions to VHDL for Abstraction of Concurrency and Communication," *Proceedings of the Sixth International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS '98)*, 301–308, July 1998.
95. Darryl Dieckman, Perry Alexander, and Philip A. Wilsey, "ACTIVE SPEC: A Framework for the Specification and Verification of Active Network Services and Security Policies," *Formal Methods in Security Protocols*, June 1998.
96. K. Swaminathan, Radharamanan Radhakrishnan, Philip A. Wilsey, and Perry Alexander, "Large Scale Active Networks Simulation," *International Workshop on Applied Parallel Computing (PARA 98)*, (LNCS 1541), B. Kagstrom J. Dongarra, E. Elmroth, and J. Wasniewski (eds), 537–542, June 1998.
97. Malolan Chetlur, Nael B. Abu-Ghazaleh, Radharamanan Radhakrishnan, and Philip A. Wilsey, "Optimizing Communication in Time Warp Simulators," *12th Workshop on Parallel and Distributed Simulation (PADS '98)*, 64–71, May 1998.
98. Peter Frey, Radharamanan Radhakrishnan, Harold W. Carter, and Philip A. Wilsey, "Optimistic Synchronization of Mixed-Mode Simulator," *12th International Parallel Processing Symposium (IPPS/SPDP '98)*, 694–699, April 1998.

99. John Penix, Dale E. Martin, Peter Frey, Radharamanan Radhakrishnan, Perry Alexander and Philip A. Wilsey, "Experiences in Verifying Parallel Simulation Algorithms," *Workshop on Formal Methods in Software Practice (FMSP-98)*, M. Ardis (ed), 16–23, March 1998.
100. Peter J. Ashenden and Philip A. Wilsey, "A Comparison of Alternative Extensions for Data Modeling in VHDL," *31th Hawaii International Conference on System Sciences (HICSS-31)*, January 1998.
101. Vijay Balakrishnan, Peter Frey, N. Abu-Ghazaleh, and Philip A. Wilsey, "A Framework for Performance Analysis of Parallel Discrete Event Simulators," *Proceedings of the 1997 Winter Simulation Conference*, S. Andradottir, K. J. Healy, D. H. Withers, and B. L. Nelson (eds), 429–436, December 1997.
102. Loy M. D'Souza, Xianzhi Fan, and Philip A. Wilsey, "Modifications to the pGVT Algorithm to Eliminate Acknowledgment Messages and Improve the GVT Broadcast Frequency," *World Congress on Systems Simulation: Conference on Parallel & Distributed Simulation*, 288–292, September 1997.
103. Peter Frey, Harold W. Carter, and Philip A. Wilsey, "Parallel Synchronization of Continuous Time and Discrete Event Simulators," *1997 International Conference on Parallel Processing*, 227–231, August 1997.
104. Murali Rangarajan, John Penix, Perry Alexander, and Philip A. Wilsey, "Gravity: An Object-Oriented Framework for Hardware/Software Tool Integration," *Proc. of the 30th Annual Simulation Symposium*, 24–30, April 1997.
105. Radharamanan Radhakrishnan, Lantz Moore, and Philip A. Wilsey, "External Adjustment of Runtime Parameters in Time Warp Synchronized Parallel Simulators," *11th International Parallel Processing Symposium (IPPS '97)*, 260–266, April 1997.
106. Philip A. Wilsey, "Feedback Control in Time Warp Synchronized Parallel Simulators," *First International Workshop on Distributed Interactive Simulation and Real Time Applications*, 31–38, January 1997.
107. Sheetanshu L. Pandey, Kothanda R. Subramanian, and Philip A. Wilsey, "A Semantic Model of VHDL for Validating Rewriting Algebras," *Proceedings of the 22nd EUROMICRO Conference (Beyond 2000: Hardware and Software Design Strategies)*, 167–176, September 1996.
108. Nael B. Abu-Ghazaleh and Philip A. Wilsey, "A Shared Control Parallel Architecture Model," *Massively Parallel Computing Systems (MPCS '96)*, 332–337, May 1996.
109. Radharamanan Radhakrishnan, Timothy J. McBrayer, Krishnan Subramani, Malolan Chetlur, Vijay Balakrishnan, and Philip A. Wilsey, "A Comparative Analysis of Various Time Warp Algorithms Implemented in the WARPED Simulation Kernel," *Proc. of the 29th Annual Simulation Symposium*, 107–116, March 1996.
110. Balakrishnan Kannikeswaran, Radharamanan Radhakrishnan, Peter Frey, Perry Alexander, and Philip A. Wilsey, "Formal Specification and Verification of the pGVT Algorithm," *FME '96: Industrial Benefit and Advances in Formal Methods*, (LNCS 1051), M-C. Gaudel and J. Woodcock (eds), 405–424, March 1996.

111. Christopher H. Young and Philip A. Wilsey, "Optimistic Fossil Collection for Time Warp Simulation," *29th Hawaii International Conference on System Sciences (HICSS-29)*, H. El-Rewini and B. D. Shriver (eds), Volume I, 364–372, January 1996.
112. Dale E. Martin, Timothy J. McBrayer, and Philip A. Wilsey, "WARPED: A Time Warp Simulation Kernel for Analysis and Application Development," *29th Hawaii International Conference on System Sciences (HICSS-29)*, H. El-Rewini and B. D. Shriver (eds), Volume I, 383–386, January 1996.
113. Sidhartha Mohanty and Philip A. Wilsey, "System Modeling, Hardware-Software Codesign, and Mixed Modeling with Hardware Description Languages," *1995 EURO-DAC Conference*, 322–327, September 1995.
114. Philip A. Wilsey, D. M. Benz, and Sheetanshu L. Pandey, "A Model of VHDL for the Analysis, Transformation, and Optimization of Digital System Designs," *Conference on Hardware Description Languages (CHDL'95)*, 611–616, August 1995.
115. L. Moore, Debra A. Hensgen, D. Charley, V. Krishnaswamy, T. McBrayer, Dale E. Martin, and Philip A. Wilsey, "graze: A Tool for Performance Visualization and Analysis," *1995 International Conference on Parallel Processing*, Volume II Software, C. Polychronopoulos (ed), II-135–II-138, August 1995.
116. Sidhartha Mohanty and Philip A. Wilsey, "Rapid System Prototyping, System Modeling, and Analysis in a Hardware-Software Codesign Environment," *IEEE Rapid Systems Prototyping Workshop*, R. Lauwereins (ed), 154–160, June 1995.
117. Josef Fleischmann and Philip A. Wilsey, "Comparative Analysis of Periodic State Saving Techniques in Time Warp Simulators," *Proc. of the 9th Workshop on Parallel and Distributed Simulation (PADS '95)*, 50–58, June 1995, doi:[10.1109/PADS.1995.404317](https://doi.org/10.1109/PADS.1995.404317)
118. Timothy J. McBrayer and Philip A. Wilsey, "Process Combination to Increase Event Granularity in Parallel Logic Simulation," *9th International Parallel Processing Symposium (IPPS '95)*, 572–578, April 1995.
119. Raghunandan Rajan and Philip A. Wilsey, "Dynamically Switching between Lazy and Aggressive Cancellation in a Time Warp Parallel Simulator," *Proc. of the 28th Annual Simulation Symposium*, 22–30, April 1995.
120. Sidhartha Mohanty, V. Krishnaswamy, and Philip A. Wilsey, "System Modeling, Performance Analysis, and Evolutionary Prototyping with Hardware Description Languages," *Proceedings of the Third International Workshop on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS '95)*, P. Dowd and E. Gelenbe (eds), 312–318, January 1995.
121. Venkatram Krishnaswamy and Philip A. Wilsey, "A Framework for Visualizing Performance Data in a Graphical Design Environment," *International Conference on Electronic Hardware Description Languages (ICEHDL)*, 29–34 January 1995.

122. Peter J. Ashenden and Philip A. Wilsey, "Polymorphic Abstract Data Types in VHDL," *International Conference on Electronic Hardware Description Languages (ICEHDL)*, 35–40, January 1995.
123. Avinash C. Palaniswamy, and Philip A. Wilsey, "Scheduling Time Warp Processes using Adaptive Control Techniques," *Proceedings of the 1994 Winter Simulation Conference*, J. D. Tew, S. Manivannan, D. A. Sadowski, and A. F. Seila (eds), 731–738, December 1994.
124. Ross A. Bagley, Philip A. Wilsey, and Nael B. Abu-Ghazaleh, "Composing Functional Unit Blocks for Efficient Interpretation of MIMD Code Sequences on SIMD Processors," *Parallel Processing: CONPAR 94 – VAPP VI*, (LNCS 854), B. Buchberger and J. Volkert (eds), 616–627, September 1994.
125. Loy M. D'Souza, Xianzhi Fan, and Philip A. Wilsey, "pGVT: An Algorithm for Accurate GVT Estimation," *Proc. of the 8th Workshop on Parallel and Distributed Simulation (PADS '94)*, 102–109, July 1994.
126. Nael B. Abu-Ghazaleh, Philip A. Wilsey, Xianzhi Fan, and Debra A. Hensgen, "Variable Instruction Issue for Efficient MIMD Interpretation on SIMD Machines," *8th International Parallel Processing Symposium (IPPS '94)*, 304–310, April 1994.
127. Philip A. Wilsey, Avinash C. Palaniswamy, and Sandeep Aji, "Rollback Relaxation: A Technique for Reducing Rollback Costs in an Optimistically Synchronized Simulation," *International Conference on Simulation and Hardware Description Languages*, 143–148, January 1994.
128. Avinash C. Palaniswamy and Philip A. Wilsey, "Adaptive Checkpoint Intervals in an Optimistically Synchronized Parallel Digital System Simulator," *VLSI 93*, 353–362, (*IFIP Transactions A: Computer Science and Technology*, Volume A-42), September 1993.
129. Avinash C. Palaniswamy and Philip A. Wilsey, "An Analytical Comparison of Periodic Checkpointing and Incremental State Saving," *Proc. of the 7th Workshop on Parallel and Distributed Simulation (PADS '93)*, Society for Computer Simulation, 127–134, July 1993.
130. Nick L. Rethman and Philip A. Wilsey, "RAPID: A Tool for Hardware/Software Tradeoff Analysis," *Proc. of the Spring 1993 VHDL Int. Users' Forum*, 91–99, April 1993.
131. Avinash C. Palaniswamy, Sandeep Aji, and Philip A. Wilsey, "Performance Measures for Several Optimizations to a Distributed Digital System Simulator," *Proc. of the 26th Annual Simulation Symposium*, 21–29, April 1993.
132. Philip A. Wilsey, Debra A. Hensgen, Nael B. Abu-Ghazaleh, Charles E. Slusher, and David Y. Hollinden, "The Concurrent Execution of Non-communicating Programs on SIMD Processors," *Proc. of the 4th Symposium on the Frontiers of Massively Parallel Computation*, 29–36, October 1992.
133. Charles E. Slusher, Philip A. Wilsey, Debra A. Hensgen, and David Y. Hollinden, "The Efficient Compilation, Loading, and Execution of Mutated Programs on SIMD Computers," *5th ISMM International Conference on Parallel and Distributed Computing and Systems*, R. Melhem (ed), 100–103, October 1992.

134. David Charley, Timothy J. McBrayer, Debra A. Hensgen, Philip A. Wilsey, and M. Ankola, “Distributed Simulation on a Reconfigurable Network using Non-Uniform Message Passing,” *5th ISMM International Conference on Parallel and Distributed Computing and Systems*, R. Melhem (ed), 247–250, October 1992.
135. Debra A. Hensgen and Philip A. Wilsey, “MINTABS: Early Experiences with a New Paradigm for Programming SIMD Computers,” *12th International Conference on Distributed Computing Systems*, 110–117, June 1992.
136. Avinash C. Palaniswamy, Sandeep Aji, and Philip A. Wilsey, “An Efficient Implementation of Lazy Reevaluation,” *Proc. of the 25th Annual Simulation Symposium*, 140–146, April 1992.
137. David Y. Hollinden, Debra A. Hensgen, and Philip A. Wilsey, “Experiences Implementing the MINTABS System on a MasPar MP-1,” *Proc. 3rd Symposium on Experiences with Distributed and Multiprocessor Systems (SEDMS III)*, 43–58, March 1992.
138. Philip A. Wilsey and Debra A. Hensgen, “Exploiting SIMD Computers for General Purpose Computation,” *6th International Parallel Processing Symposium (IPPS ’92)*, 675–679, March 1992.
139. Norman Mause and Philip A. Wilsey, “The Impact of Abstract Data Types on the Digital System Description and Simulation Environments,” *European Conference on Design Automation*, 88–96, March 1992.
140. Philip A. Wilsey and Norman Mause, “The Use of Abstract Data Types in Hardware Description Languages,” *Proc. of the 1992 Western Simulation Multiconference on Simulation in Engineering Education*, 101–105, January 1992.
141. Philip A. Wilsey, Timothy J. McBrayer, and David Sims, “Towards A Formal Model of VLSI Systems Compatible with VHDL,” *VLSI 91*, 225–236, (*IFIP Transactions A: Computer Science and Technology*, Volume A-1), August 1991.
142. Harold W. Carter, Ranga Vemuri, Philip A. Wilsey, Jim Aylor, Ron Waxman, and Tom Hartrum, “High Speed Acceleration of VHDL Simulation, Synthesis, and ATPG: Overview of the QUEST Project,” *Spring 1991 VHDL Users’ Group*, 85–90, April 1991.
143. Philip A. Wilsey, “Introducing the Notion of Clocks into the VHDL Description Environment,” *Spring 1991 VHDL Users’ Group*, 97–104, April 1991.
144. Philip A. Wilsey, “Developing a Formal Semantic Definition of VHDL,” *The First European Working Conf. on VHDL Methods*, September 1990.
145. Philip A. Wilsey, “The Use of Interval Temporal Logic in Specifying Relationships Between Clock Phases,” *Int. Workshop on Timing Issues in the Specification and Synthesis of Digital Systems*, August 1990.
146. Praveen Chawla, Harold W. Carter, and Philip A. Wilsey, “An Investigation of the Performance of a Distributed Functional Digital Simulator,” *Proc. of the 32nd Midwest Symposium on Circuits and Systems*, 470–476, August 1989.

147. Philip A. Wilsey, “Computer Architecture Specification with Interval Temporal Logic,” *Proc. 9th International Symp. on Computer Hardware Description Languages*, J. A. Darringer & Franz J. Ramming (Ed.), 35–45, June 1989.
148. Philip A. Wilsey and Subrata Dasgupta, “Functional and Operational Specifications of Computer Architectures,” *Proc. 9th International Symp. on Computer Hardware Description Languages*, J. A. Darringer & Franz J. Ramming (Ed.), 209–223, June 1989.
149. Philip A. Wilsey and Subrata Dasgupta, “A Formal Model of Computer Architectures for Computer System Design Environments,” in *Design Methodologies for VLSI and Computer Architecture*, D. A. Edwards (Ed.), 263–278, Sept. 1988.

Other Articles & Technical Reports

1. Jonathan Lockhart, Carla Purdy and Philip A. Wilsey, “Error Analysis and Reliability Metrics for Software in Safety Critical Systems,” *IEEE 61th International Midwest Symposium on Circuits and Systems*, 2018. doi:[10.1109/MWSCAS.2018.8624052](https://doi.org/10.1109/MWSCAS.2018.8624052)
2. Jonathan Lockhart, Carla Purdy and Philip A. Wilsey, “Error Discovery and Removal in Embedded Systems Software via Automated Theorem Proving,” *NAECON '17*, June 2017. doi:[10.1109/NAECON.2017.8268802](https://doi.org/10.1109/NAECON.2017.8268802)
3. Jonathan Lockhart, Carla Purdy and Philip A. Wilsey, “Error Analysis and Reliability Metrics for Software in Safety Critical Systems,” *IEEE 59th International Midwest Symposium on Circuits and Systems*, 2016, doi:[10.1109/MWSCAS.2016.7869962](https://doi.org/10.1109/MWSCAS.2016.7869962)
4. Vignesh Subbian, Philip A. Wilsey, and Fred R. Beyette, Jr., “Design and Development of a Robotic System for Clinical Assessment of Motor Deficits,” *IEEE 56th International Midwest Symposium on Circuits and Systems*, 2013.
5. S. B. Kim, S. J. Cho, and Philip A. Wilsey, “Tackling Basic Block-based Anomaly Execution Behaviors,” *2008 International Symposium on Consumer Electronics*, 2008.
6. Dmitry A. Gorodetsky and Philip A. Wilsey, “Large scale parallel FDTD computations aided by modal decomposition,” Technical Report ECECS-TR-2006-2, Department of Electrical and Computer Engineering, University of Cincinnati, 2006.
7. Dmitry A. Gorodetsky and Philip A. Wilsey, “Parallel finite-difference time-domain computations aided by modal decomposition,” *SIAM Conference on Parallel Processing for Scientific Computing*, 2006.
8. Dmitry A. Gorodetsky and Philip A. Wilsey, “Reduction of FDTD simulation time with modal methods,” *Progress in Electromagnetics Research Symposium*, 2006.
9. Douglas R. Hickey, Chris J. Fearing, Fred R. Beyette Jr, and Philip A. Wilsey, “Characterization of the PHOCI(TM) Data Integrated Video Sensor Technology,” *IEEE International Midwest Symposium on Circuits and Systems*, 380–383, 2005.

10. Dmitry A. Gorodetsky and Philip A. Wilsey, "A signal processing approach to finite-difference time-domain computations," *IEEE International Midwest Symposium on Circuits and Systems*, 2005.
11. Chris J. Fearing, Douglas R. Hickey, Philip A. Wilsey, and Karen Tomko, "Performance Issues in the Implementation of the M-VIA Communication Software," *PARCO 2003*, 509–516, Sept 2003.
12. Greg D. Peterson and Philip A. Wilsey, "Requirements for Object-Oriented Systems Modeling with STEAMS," *2001 IEEE International Workshop on Behavioral Modeling and Simulation (BMAS)*, pp 45–52, Oct 2001.
13. Radharamanan Radhakrishnan and Philip A. Wilsey, "Fine-tuning Optimistic Simulation," *IEEE Potentials*, 30–33, Vol 19, No 2, April/May 2000.
14. Radharamanan Radhakrishnan and Philip A. Wilsey, "Parallel Simulation of Mixed-Technology Electronic Systems," TR 250–10–99–ECECS, Dept. of Electrical and Computer Engineering and Computer Science, University of Cincinnati, Cincinnati, OH, October 1999.
15. Peter J. Ashenden, Philip A. Wilsey and Dale E. Martin, "SUAVE Language Description", Technical Report 99/04, Dept Computer Science, The University of Adelaide, July 1999.
16. Darryl Dieckman, Dale E. Martin, and Philip A. Wilsey, "Distributed Web-Based Simulation for Protecting Intellectual Property," *Proceedings of SPIE's 13th Annual International Symposium on Aerospace/Defense Sensing, Simulation, and Controls*, April 1999.
17. Nael B. Abu-Ghazaleh, Philip A. Wilsey, J. Potter, R. Walker, and J. Baker, "Flexible Parallel Processing in Memory: Architecture + Programming Model," *The Third PetaFlop Workshop*, held in conjunction with Frontiers on Massively Parallel Computing, February 1999.
18. Darryl Dieckman, Dale E. Martin, and Philip A. Wilsey, "DISCOE: Distributed Design & Analysis to Preserve Intellectual Property," *IEEE Information Technology Conference*, 57–60, September 1998.
19. Peter J. Ashenden, Philip A. Wilsey, and Dale E. Martin, "SUAVE: Object-Oriented and Genericity Extensions to VHDL for High-Level Modeling," *Forum on Design Languages*, September 1998.
20. Philip A. Wilsey, Dale E. Martin, and Herb Hirsch, "The SAVANT Project," *NAECON '98*, June 1998.
21. John C. Willis, Philip A. Wilsey, and Greg D. Peterson, "Advanced Intermediate Representation with Extensibility," Tutorial at the *VHDL Users' Group Spring 1998 Conference*, March 1998.
22. Philip A. Wilsey, Dale E. Martin, and Krishnan Subramani, "SAVANT/TyVIS/WARPED: Components for the Analysis and Simulation of VHDL," *VHDL Users' Group Spring 1998 Conference*, 195–201, March 1998.
23. Peter J. Ashenden and Philip A. Wilsey, "Considerations on System-Level Behavioural and Structural Modeling Extensions to VHDL," *VHDL Users' Group Spring 1998 Conference*, 42–50, March 1998. (best paper).

24. Peter J. Ashenden, Philip A. Wilsey, and Dale E. Martin, “Reuse Through Genericity in SUAVE,” *VHDL Users’ Group Fall 1997 Conference*, 170–177, October 1997.
25. Peter J. Ashenden, Philip A. Wilsey, and Dale E. Martin, “SUAVE: Painless Extension for an Object-Oriented VHDL,” *VHDL Users’ Group Fall 1997 Conference*, 60–67, October 1997.
26. Peter J. Ashenden, Philip A. Wilsey, and Dale E. Martin, “SUAVE: A Proposal for Extensions to VHDL for High-Level Modeling,” Joint Technical Report, TR—7/97, Dept. Computer Science, University of Adelaide and TR–207/08/97/ECECS, Department of Electrical & Computer Engineering and Computer Science, University of Cincinnati, August 1997.
27. Peter J. Ashenden and Philip A. Wilsey, “Principles for Language Extension to VHDL to Support High-Level Modeling,” Joint Technical Report TR–03/97, Dept. Computer Science, University of Adelaide and TR–204/05/97/ECECS, Department of Electrical & Computer Engineering and Computer Science, University of Cincinnati, May 1997.
28. Peter J. Ashenden and Philip A. Wilsey, “A Comparison of Alternative Extensions for Data Modeling in VHDL,” Joint Technical Report TR–02/97, Dept. Computer Science, University of Adelaide and TR–203/05/97/ECECS, Department of Electrical & Computer Engineering and Computer Science, University of Cincinnati, May 1997.
29. Peter J. Ashenden and Philip A. Wilsey, “Considerations on Object-Oriented Extensions to VHDL,” *VHDL Users’ Group Spring 1997 Conference*, 109–118, March 1997.
30. Loy M. D’Souza, Xianzhi Fan, and Philip A. Wilsey, “Modifications to the pGVT Algorithm to Eliminate Acknowledgment Messages and Improve the GVT Broadcast Frequency,” Tech. Rept. TR 201–12–96–ECECS, Dept. of Electrical & Computer Engineering and Computer Science, University of Cincinnati, November 1996.
31. Dale E. Martin and Philip A. Wilsey, “SIMD Processors: Expanding its Horizons by Emulating Others,” *IEEE Potentials*, 26–28, October/November 1996.
32. John C. Willis, Philip A. Wilsey, Greg D. Peterson, John Hines, W. H. Dashiell, “Semi-Automated Validation of VHDL and Related Languages,” *VHDL Users’ Group Fall 1996 Conference*, 335–342, October 1996. Reprinted in *VHDL: Electronic Systems Design Methodologies and Interactive Tutorial*, IEEE Press, 2000.
33. John C. Willis, Philip A. Wilsey, Greg D. Peterson, John Hines, A. Zamfirescu, Dale E. Martin, and R. N. Newshutz, “Advanced Intermediate Representation with Extensibility (AIRE),” *VHDL Users’ Group Fall 1996 Conference*, 33–40, October 1996. Reprinted in *VHDL: Electronic Systems Design Methodologies and Interactive Tutorial*, IEEE Press, 2000.
34. Philip A. Wilsey, Sheetanshu L. Pandey, and Kothanda Umamageswaran, “A Formal Model of Digital Systems Compatible with VHDL,” *RASSP Digest*, Volume 3, 46–48, September 1996.
35. Dale E. Martin, Philip A. Wilsey, and Praveen Chawla, “SAVANT: An Extensible Object-Oriented Intermediate for VHDL,” *VHDL Users’ Group Spring 1996 Conference*, 275–281, March 1996.

36. Philip A. Wilsey and Dale E. Martin, "Coordinating Joint Cost/No-Cost Rights for Software Developed with SBIR Funding," *First Conference on Freely Redistributable Software*, 89–94, February 1996.
37. Sidhartha Mohanty, Shane M. Gunning, and Philip A. Wilsey, "Abstract Modeling and Performance Analysis with HDLs," *Third Asia Pacific Conference on Hardware Description Languages (APCHDL'96)*, 13–17, January 1996.
38. Dale E. Martin, Timothy J. McBrayer, and Philip A. Wilsey, "Time Warp Parallel Simulation of VHDL Descriptions and the need for Dynamic Parameter Adjustment," *VHDL Users' Group Fall 1995 Conference*, 7.1–7.10, October, 1995.
39. Nael B. Abu-Ghazaleh, Philip A. Wilsey, and Ross A. Bagley, "Shared Control Multiprocessors," TR 179–12–94–ECECS, Dept. of Electrical and Computer Engineering and Computer Science, University of Cincinnati, Cincinnati, OH, December 1994.
40. Timothy J. McBrayer, V. Krishnaswamy, Sidhartha Mohanty, Lantz Moore, X. Liu, J. Carter, David Charley, Philip A. Wilsey, Debra A. Hensgen, Harold W. Carter, P. Chawla, J. Collier and S. Bilik, "VAST: Time Warp Simulation of VHDL on SMP Workstations," *VHDL Users' Group Fall 1994 Conference*, 4.7–4.16, November, 1994.
41. Lantz Moore, Debra A. Hensgen, V. Krishnaswamy, and Philip A. Wilsey, "The Importance of Dynamic Memory Allocation in Threaded Applications," Tech. Rept. TR 171–6–94–ECE, Dept. of Electrical & Computer Engineering, University of Cincinnati, June 1994.
42. Xianzhi Fan, Philip A. Wilsey, and Nael B. Abu-Ghazaleh, "On the Complexity of Optimizing Instruction Issue Cycles for SIMD Interpretation of MIMD," Tech. Rept. TR 170–6–94–ECE, Dept. of Electrical & Computer Engineering, University of Cincinnati, June 20, 1994.
43. Philip A. Wilsey, "Post-conference Report: 1994 Simulation MultiConference," *Simulation*, Volume 62, Number 6, 392–393, June 1994.
44. Philip A. Wilsey and Sidhartha Mohanty, "VHDL Design Libraries for Rapid System Prototyping," *Proc. of the 1994 Western Simulation Multiconference on Simulation in Engineering Education*, Society for Computer Simulation, 35–40, January 1994.
45. Philip A. Wilsey, "Post-conference Report: 1993 Western Multiconference on Computer Simulation," *Robotica*, Volume 11, Part 5, pg 483, September–October, 1993.
46. Sandeep Aji, Avinash C. Palaniswamy, and Philip A. Wilsey, "Interactions of Optimizations to a Time Warp Synchronized Digital System Simulator," *Modeling and Simulation (FSM 93)*, 593–597, June 1993.
47. Avinash C. Palaniswamy and Philip A. Wilsey, "Adaptive Bounded Time Windows in an Optimistically Synchronized Simulator," *Third Great Lakes Symposium on VLSI*, 114–118, March 1993.
48. Nael B. Abu-Ghazaleh, T. Dichiario, Philip A. Wilsey, Debra A. Hensgen, and Marc M. Cahay, "Parallel Execution of Monte Carlo Simulations on SIMD Processors," Tech. Rept. TR 146–2–93–ECE, Dept. of Electrical & Computer Engineering, University of Cincinnati, February 1993.

49. Philip A. Wilsey, Ranga Vemuri, and Norman Mause, “Instrumenting the Digital System Simulation Environment” *Proc. of the 1993 Western Simulation Multiconference on Simulation in Engineering Education*, 224–229, January 1993.
50. D. Charley, Debra A. Hensgen, T. McBrayer, Philip A. Wilsey, and M. Ankola, “High Speed Communication for Simulation of Large VHDL Models,” *Proc. of the Fall 1992 VHDL Int. Users’ Forum*, 212–216, October 1992.
51. Thomas J. McBrayer, David Charley, Philip A. Wilsey, and Debra A. Hensgen, “A Parallel, Optimistically Synchronized VHDL Simulator Executing on a Network of Workstations,” *Proc. of the Fall 1992 VHDL Int. Users’ Forum*, 218–222, October 1992.
52. Philip A. Wilsey and Avinash C. Palaniswamy, “Rollback Relaxation,” Tech. Rept. TR 135–2–92–ECE, Dept. of Electrical & Computer Engineering, University of Cincinnati, February 1992.
53. Philip A. Wilsey, “Book Review of *The Art of Computer Systems Performance Analysis Techniques for Experimental Design, Measurement, Simulation, and Modeling*, by R. Jain,” *International Journal in Computer Simulation*, Volume 1, Number 4, pg 427, 1991.
54. Philip A. Wilsey, Debra A. Hensgen, Charles E. Slusher, Nael B. Abu-Ghazaleh, and David Y. Hollinden, “Exploiting SIMD Computers for Mutant Program Execution,” Tech. Rept. TR 133–11–91–ECE, Dept. of Electrical & Computer Engineering, University of Cincinnati, November 1991.
55. P. Chawla and P.A. Wilsey, “Synchronizing Distributed VHDL Simulation,” Tech. Rept. TR 131–4–91–ECE, Dept. of Electrical & Computer Engineering, University of Cincinnati, April 1991.
56. Philip A. Wilsey and Subrata Dasgupta, “Compiling Axiomatic Descriptions for Imperative Execution,” Tech. Rept. TR87–5–1, The Center for Advanced Computer Studies, University of Southwestern Louisiana, July 1987.
57. Philip A. Wilsey, Mike T. Wright, Subrata Dasgupta, Juha Heinanen, and J. Wang, “An S*M Execution Environment,” Tech. Rept. TR87–3–1, The Center for Advanced Computer Studies, University of Southwestern Louisiana, February 1987.

Awards

- CEAS Distinguished Engineering Researcher Award, College of Engineering and Applied Science, 2023.
- CEAS Distinguished Research Award, College of Engineering and Applied Science, 2018.
- Eta Kappa Nu, Outstanding Professor of the Year, Dept. of Electrical and Computer Engineering, 2012–2013.
- William H. Middendorf Research Excellence Award, Dept. of Electrical & Computer Engineering and Computer Science, 1995–96.

- Distinguished Progress in Teaching Excellence Award, Dept. of Electrical and Computer Engineering, 1987–88.

Honors

- Listed in Dictionary of International Biography, 1999.
- Listed in Who's Who in America, 1999–2009.
- Listed in Who's Who in Science and Engineering, 1994–2009.
- Listed in Who's Who in the World, 1998–2009.
- Listed in Who's Who in American Education, 2004–2007.
- Listed in International Who's Who of Information Technology, 1999.
- Listed in Who's Who in the Midwest, 1994–95, 95–96, 97–98.
- Listed in Who's Who in the Media and Communications, 1998–99.
- Listed in Who's Who in Finance and Industry, 1997–98, 98–99.
- Listed in 5000 Personalities of the World, 1996.

Activities

Professional Activities

1. Referee for *Software: Practice and Experience* 2022, *Computer Languages* 2001, *ACM Transactions on Design Automation of Electronic Systems* (1999), *ACM Transactions on Modeling and Computer Simulation* (TOMACS) (1996–98, 2001, 2005, 2006, 2013–2018), *IEEE Computer* (1987–98), *IEEE Journal on Selected Areas in Communications* (2000), *IEEE Parallel and Distributed Technology* (1992, 1994, 1995), *IEEE Software* (1985–89), *IEEE Transactions on Computers* (1990–94, 2000–01), *IEEE Transactions on Computer Aided Design* (2001), *IEEE Transactions on Software Engineering* (1996), *IEEE Transactions on Parallel and Distributed Systems* (1995, 1996, 2010–2018), *International Journal in Computer Simulation* (1990–96), *Journal of Computer & Software Engineering* (1994), *Journal of Parallel and Distributed Computing* (JPDC) (1992, 1994–96, 1999, 2000, 2005, 2006), *Transactions of The Society for Computer Simulation* (1995–97, 2000–2017), *VLSI Design* (1992, 1995, 1996), Annual Simulation Symposia (1992–06), Annual Workshop on Microprogramming and Microarchitecture (1990–93), Annual International Conference on Parallel Processing (ICPP) (1989, 1990, 1995), Annual Workshop on Microprogramming (1986–1987), GLOBECOM '95, Hawaii International Conference on System Sciences (HICSS) (1992, 1996, 1998), IEEE Symposium on Parallel and Distributed Processing (SPDP) (1995), International Parallel Processing Symposium (IPPS) (1994–99), International Symposium on Computer Architecture (1992), International Symposia on Computer Hardware Description Languages (1986), ISMM International Conference on

- Parallel and Distributed Computing and Systems (1992, 1995), Winter Simulation Conference (1994, 1996), Workshop on Parallel and Distributed Simulation (PADS) (1994–15), and World Congress on Systems Simulation, Conference on Parallel & Distributed Simulation (1997).
2. Member, CSAB evaluation teams (1998–2003).
 3. Reviewer of grant proposals: NSF (1991, 1994, 2000, 2001, 2020, 2022); NSF Panelist (2001, 2002, 2019).
 4. Opponent, PhD theses: M. Liljenstam, Royal Institute of Technology (2000). External reviewer, PhD theses: S. Conoci, , University of Roma (2021); X. Yadong, Nanyang Technological University (2017); A. Pellegrini, University of Roma (2014); J. Jin, Nanyang Technological University (2014); L. Li, McGill University (2008); A. Santoro, University of Rome (2002); P. Ashenden, University of Adelaide (1997); H. Avril, McGill University (1996); R. Singh, Wright State University (1996–97).
 5. Book Reviewer for *Wiley* (2006), *Addison-Wesley* (2000), *Kluwer Academic Publishers* (1997), *McGraw-Hill, Inc* (1993–1996, 2008), *West Educational Publishing* (1993, 1994), *Morgan Kaufmann* (1991, 1992, 1994, 1996), and *Prentice Hall* (1990).
 6. Editor-in-Chief: *IEEE Potentials Magazine*, 1999–03; Guest Editor: Special Issue of *ACM Transactions on Modeling and Computer Simulation*, 2018; Area Editor: *Transactions of the Society for Modeling and Simulation International*, 2003–; Associate Editor: *IEEE Access*, 2013–2016; *International Journal of Simulation and Process Modeling (IJSPM)* 2004–; *Transactions of the Society for Modeling and Simulation International*, 2001–2003; *IEEE Potentials Magazine*, 1992–99, 03–10; Member, Editorial Advisory Board, *VLSI Design*, Gordon and Breach Science Publishers, 1993–2002.
 7. IEEE Committees: IEEE Cincinnati Region, Executive Comm (1998–); Publications Products and Services Board (1999–2002); Regional Activities Board (non-voting) (1999–2002); and Student Activities Committee (1999–2002).
 8. Steering Committees: Chair, Workshop on Parallel and Distributed Simulation (PADS), 2000–2003.
 9. Keynotes: PhD Colloquium, ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS), 2017.
 10. General Chair: Workshop on Parallel and Distributed Simulation (PADS), 2002 (Co-Chair); The 29th Annual Simulation Symposium, 1996; The International Conference on Electronic Hardware Description Languages (ICEHDL), 1995; The International Conference on Simulation and Hardware Description Languages, 1994. Associate General Chair: The SCS Winter Simulation Multiconference, (1993, 1994);
 11. Program Chair: ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS), 2001, 2003 (Co-Chair), 2017 (Co-Chair); Workshop on Massively Parallel Processing (WMPP), 2001–2002, 2003–2004 (Co-Chair); Workshop on Distributed Simulation and Real-Time Applications (DS-RT), 2001 (Co-Chair); International Conference On Web-Based Modeling & Simulation, WEBSIM 2000; MASCOTS '98 (Co-Chair); Fall 1997 VHDL International Users' Forum, 1997; The 26th Annual Simulation Symposium, 1993; The 25th Annual Simulation Symposium, 1992.

12. Tutorials Chair: DS-RT 2022 (Co-Chair); MASCOTS 2001.
13. Track Coordinator: High Performance Computing and Simulation 2021; Advanced Tutorials, Winter Simulation Conference 2014 (w/ Helen D. Karatza), 2015, and 2017.
14. Program Committees: Theory and Foundations for Modeling and Simulation (TMS), 2021-23; Workshop on the Performance and Energy-efficiency of Concurrent Systems (PECS), 2021-22; Spring Simulation Conference, 2020; Winter Simulation Conference, 2014-17, 19-21; International Conference on Simulation Tools and Techniques, (SIMULTOOLS), 2017; International Conference on System Simulation (AsiaSim), 2013; International Conference on Embedded and Multimedia Computing (EMC), 2012-14; International Conference on Advances in System Simulation (SIMUL), 2012-17; International Conference on Simulation and Modeling Methodologies, Technologies, and Applications (SIMULTECH), 2011-21; Distributed and Parallel Systems Track of IEEE AINA, (2011); Parallel Computing (ParCo), 2005; Modeling and Simulation of Multi-Agent Systems, (AAMAS 04); Symposium on Distributed Simulation and Real Time Applications DS-RT (formerly DIS-RT), (2004-23); Communications Networks and Distributed Systems Modeling and Simulation Conference, (2002); International Performance, Computing and Communications Conference, (2002); International Conference on Grand Challenges for Modeling and Simulation, (2002); Asian-Pacific Symposium on Cluster Computing, (2000); International Conference On Web-Based Modeling & Simulation, WEBSIM (1999-2000); MASCOTS, (1998-2001); VLSI Design, 1998; World Congress on Systems Simulation, Conference on Parallel & Distributed Simulation, (1997); Workshop on Parallel and Distributed Simulation (PADS), (1996-23); Spring VHDL Int. Users' Forum, (1994, 1995, 1998); Fall VHDL Int. Users' Forum, (1993, 1995, 1997, 1998); Annual Simulation Symposia, (1992-97, 2000-07); Annual Workshop on Microprogramming and Microarchitecture, (1990, 1991); Winter Simulation Conference, 2019.
15. Session Chair: SIMUL 2011; ICCP 2011; International Conference On Web-Based Modeling & Simulation, WEBSIM 1999; Distributed Interactive Simulation and Real-Time Applications (DS RT), 1997; 2019; Workshop on Parallel and Distributed Simulation (PADS), (1993, 1996, 1998, 2006, 2008, 2013, 2016); Winter Simulation Conference, 1994; Annual Simulation Symposia, (1992, 1994-97, 2004-07); 23rd Annual Workshop on Microprogramming and Microarchitecture (MICRO-23), 1990.
16. Panels: Winter Simulation Conference (Moderator), 2002; International Conference On Web-Based Modeling & Simulation, 1999; Distributed Interactive Simulation and Real-Time Applications, 1997; VIUF, Durham, NC, Oct. 1996 (moderator); Workshop on Parallel and Distributed Simulation (PADS), Lake Placid, NY, 1995; SIGCSE, Washington, D.C., Feb. 1990.
17. Lecture/Seminars: Iowa State University, Ames IA, February 2002; University of Connecticut, Storrs CT, November 2001; University of Texas, Dallas TX, May 2001; University of Georgia, Athens, GA, March 2000; University of Kansas, Lawrence, KS, March 2000; University of Louisville, Louisville, KY, October 1998; Honeywell, Minneapolis, MN, August 1998; Washington University, St Louis, MO, September 1997; University of Missouri at Rolla, Rolla, MO, September 1997; CMU, Pittsburgh, PA, March 1995; SUNY at Buffalo, Buffalo, NY, March 1995; University of Missouri at Rolla, Rolla, MO, September 1994; Technical University of Munich, Munich Germany, September 1994; Ohio Advanced Technology Center, Dayton, OH, June 1994; University of Minnesota, Minneapolis/St.

Paul MN, January 1994; IBM, Rochester, MN, January 1994; MasPar Computer Corp., Sunnyvale, CA, Jan. 1992; Columbus State Community College, Columbus, OH, Nov. 1988; University of Cincinnati, Cincinnati, OH, Jan. 1988; University of Cincinnati, Cincinnati, OH, June 1987; Marquette University, Milwaukee, WI, May 1987; Colorado State University, Fort Collins, CO, April 1987.

18. Member, IEEE Cincinnati Section Executive Committee (2005–).
19. Society Memberships: Association for Computing Machinery (ACM) (1982–2009): ACM Special Interest Group on Microprogramming (SIGMICRO), ACM Special Interest Group on Computer Architecture (SIGARCH), ACM Special Interest Group on Programming Languages (SIGPLAN), ACM Special Interest Group on Design Automation (SIGDA); IEEE (Member 1982–98, Senior Member 98–): IEEE Computer Society; American Association for the Advancement of Science (AAAS) (1990–96); Design Automation Standards Committee (DASC) (1996–2008); Issues Screening and Analysis Committee (ISAC) (1997–2008); VHDL Parallel Simulation Working Group (1995–2007); Object-Oriented VHDL Study Group (1996–2004).

Academic Committees

1. Participant, Effective Teaching Institute (93-94).
2. Member, Dean's Advisory Committee, College of Engineering and Applied Science (2021–); Chair (2016–2017)/Member (2013–2016), Reappointment, Promotion, and Tenure, College of Engineering and Applied Science (2013–2017); Alternate Member, Reappointment, Promotion, and Tenure, College of Engineering and Applied Science (2011–2012); Member, All-University Graduate Faculty, University of Cincinnati (1991–); Member, Science & Engineering Research Building Planning Committee, University of Cincinnati (1990–93); Member, Information Technology Academic Advisory Committee, University of Cincinnati (1990–98); Member, Instructional Computing Sub-Committee, University of Cincinnati (1989).
3. Member, Graduate Center for Computing Disciplines (GCCD), University of Cincinnati (1988–90); Member, Hardware/Software Committee, Graduate Center for Computing Disciplines, University of Cincinnati (1988–90).
4. Member, Academic Standards Committee, College of Engineering and Applied Science, University of Cincinnati (2005–); Member, Committee to Establish the Academy of Engineering Scholars, College of Engineering, University of Cincinnati (1989); Member, Computer Director Search Committee, College of Engineering, University of Cincinnati (1989); Member, College of Engineering Computer Committee, University of Cincinnati (1988–1989); Member, College of Engineering Awards Committee, University of Cincinnati (1988, 1989).
5. Coordinator, Computer Engineering (2010-2013), School of Electric and Computing Systems, University of Cincinnati.
6. Coordinator, Experimental Systems Group (01/03–2016), Member, VLSI Design Group (01/03–), School of Electric and Computing Systems (formerly Dept of Electrical and Computer Engineering & Computer Science), University of Cincinnati.

7. Co-Director, Center for Digital Systems Engineering, Department of Electrical and Computer Engineering, University of Cincinnati (1988–1989).
8. Group Leader, Computer Systems Design Group, Department of Electrical and Computer Engineering, University of Cincinnati (1989–93).
9. Director, Departmental Computer Operations, Department of Electrical and Computer Engineering, University of Cincinnati (1988–1989).
10. Faculty Advisor, Google Developer Student Club, Department of Electrical Engineering and Computer Science, University of Cincinnati (2021–).
11. Faculty Advisor (1998–2019), Faculty Co-Advisor (2020–), IEEE, Department of Electrical and Computer Engineering, University of Cincinnati (1998–).
12. Faculty Advisor, Eta Kappa Nu, Department of Electrical and Computer Engineering, University of Cincinnati (1987–91; 2017–).
13. Member, Computer Engineering Faculty Advisors for class of 2003, Department of Electrical and Computer Engineering & Computer Science, University of Cincinnati (1999–03); Member, Faculty Search Committee, Department of Electrical and Computer Engineering & Computer Science, University of Cincinnati (1998–99); Member, Faculty Search Committee, Computer Engineering, Department of Electrical and Computer Engineering, University of Cincinnati (1996–97); Member, Evaluating Real World Impact of Research, Department of Electrical and Computer Engineering, University of Cincinnati (1995); Member, Graduate Student Awards Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1992); Member, Undergraduate Awards Selection, Department of Electrical and Computer Engineering, University of Cincinnati (1991); Member, Dept Head Review Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1990); Member, Academic Leave Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1990); Member, Mission/Vision Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1989–90); Member, Ad Hoc CS & ECE Merger Feasibility Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1989–90); Member, Departmental Awards Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1989, 1997); Chairman, Departmental Local Area Networking Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1988); Chairman, System Administrator Search Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1988); Member, Faculty Search Committee, Computer Systems Design Group, Department of Electrical and Computer Engineering, University of Cincinnati (1987–96); Member, Graduate Computer Engineering Curriculum Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1987–02); Member, Undergraduate Computer Engineering Curriculum Committee, Department of Electrical and Computer Engineering, University of Cincinnati (1987–);
14. Member, Graduate Council, University of Southwestern Louisiana, (1986–1987); Member, Student Appeals Committee (Graduate School), University of Southwestern Louisiana, (1986–1987).

Community Service

1. Member 2004–2006, (Member-at-Large 2004–2005, Treasurer 2005-2006) Board of Trustees, Carpenters Creek Civic Association.
2. Editor 2003–2005, Newsletter, Carpenters Creek Civic Association.
3. Member 1998–2000, (Secretary 1998–1999, Vice-President 1999-2000) Board of Trustees, Carpenters Creek Civic Association.

Research Grants

1. Principal Investigator, “Collaborative Research: FMitF: Track I: Formal Methods and the Computation of Persistent Homology,” National Science Foundation, 10/2023–9/2027, \$375,000, (with University of Kansas, total award \$750K). (submitted)
2. Principal Investigator, “III: Small: Strategies for Computing Persistent Homology on High Dimensional Data,” National Science Foundation, 07/2023–9/2026, \$600,000. (submitted)
3. Principal Investigator, “III: Small: Partitioning Big Data for the High Performance Computation of Persistent Homology,” National Science Foundation, 10/2019–9/2023, \$499,344.
4. Principal Investigator, “SI2-SSE: Scalable Big Data Clustering by Random Projection Hashing,” National Science Foundation, 9/2014–8/2019, \$498,127.
5. Co-Principle Investigator, “Parallel Discrete Event Simulation on Emerging High-performance Multi-core Clusters,” subcontracting UC-Riverside to the Air Force Office of Scientific Research, 10/2015–09/2019, \$299,824.
6. Co-Investigator, “Validation and Demonstration of Point-of-Care Sensor for Multi-Metal Exposure Assessment,” National Institutes of Health, 3/15–2/17, \$425,000 with I. Papautsky (PI).
7. Principal Investigator, “Studies to Minimize Failures in Embedded Systems Software,” L-3 Fuzing and Ordnance Systems, 9/2011–9/2014, \$312,587, (with Co-Investigator C. Purdy).
8. Principal Investigator, “CSR: Small: Collaborative Research: Combining Static Analysis and Dynamic Run-time Optimizations for Parallel Discrete Event Simulation in Many-Core Environments,” National Science Foundation, 8/2009–7/2014, \$166,911, collaborative project with SUNY@Binghamton.
9. Co-Investigator, “Point of Care Center for Emerging Neurotechnologies (POC-CENT),” National Institutes of Health, 7/2007–6/2014, \$9,363,719, with R. F. Beyette Jr. (PI).
10. Co-Investigator, “BEKIN: Mechanical Device Development to Assist in Assessing Neurologic Health,” NIH/POC-CENT, 7/2012–6/2013, \$223,519, with R. F. Beyette Jr. (PI).
11. Co-Investigator, “Arrayed Volatile Organic Sensors: System-Level Proof of Concept,” Air Force Research Labs, 8/2012–7/2013, \$103,000, with R. F. Beyette Jr., S. Choi, J. Heikenfeld (PI), and I. Papautsky.
12. Principal Investigator, “College Themed Door Chime,” HeathCo, LLC (a subsidiary of The Duchossios Group), 2/1/2012–7/31/2012, \$50,500.
13. Co-Investigator (7/1/10-6/30/11; Co-PI 7/1/11-6/30/13), “Novel Portable Stroke Diagnosis/Monitoring with Electrical Impedance Spectroscopy,” NIH/POC-CENT, 2010–2013, \$251,000, with R. F. Beyette Jr. (PI), collaborative project with Massachusetts General Hospital (M. Lev, PI).
14. Principal Investigator, “Hardware and Software Solutions to Support Photometric Analysis of Human CSF,” NIH/POC-CENT, 2007–2010, \$836,186, with R. F. Beyette Jr. (Co-PI).

15. Co-Investigator, “An Infrastructure for Research In Data Management, Processing, Sensing, and Visualization,” Wright State University (WCI), 2006–2008, \$449,986 (responsible for \$98,236), with H. Carter (PI). (equipment grant).
16. Co-Investigator, “MECS: Mobile Embedded Component Suite,” Army Research Laboratory, 2006–2007, \$69,000, with B. Anderson (PI).
17. Co-Investigator, “Acquisition of Research Instrumentation for Electronic Systems Emulation, Prototyping and Testing,” National Science Foundation, 2004–2007, \$280,085, with R. Vemuri (PI), H. Carter, K. Tomko, W-B. Jone, C. Purdy, and R. F. Beyette.
18. Principal Investigator, “Parallel Simulation of Mixed-Technology Models with Xyce and SAVANT,” Sandia National Labs, 2002–2006, \$476,282.
19. Co-Principal Investigator, “Collaborative Research: Parallel Reduced Order Modeling with In-Situ Error Mitigation and Performance Optimization,” National Science Foundation, 7/1/03–06/30/04, \$61,830, with K. Tomko (PI).
20. Principal Investigator, “Improving Efficiency of Simulations using Dynamic Component Substitution”, Ohio Board of Regents, 2002–2003, \$22,500.
21. Co-Investigator, “PHOCI: Photonic Communications Imager,” Army Research Office, 2003-05, \$749,000; SBIR Phase II, (D. Martin, PI).
22. Principal Investigator, “Study of Dynamic Component Substitution”, Ohio Board of Regents, 2001–2002, \$19,200.
23. Co-Investigator, “PHOCI: Photonic Communications Imager,” Army Research Office, 2002, \$69,953; SBIR with option task \$49,964, (D. Martin, PI).
24. Co-Investigator, “Mixed Signal Modeling for System Level Simulation,” Joint AFRL/DAGSI Research Program, 2001–2003, \$398,113, (R. Vemuri, PI).
25. Principal Investigator, “Distributed Simulation of Mixed-Technology for Joint Battlespace Infosphere,” Air Force, 2000–2002, \$50,000.
26. Co-Investigator, “ELASTIC: A Framework for Constructing Self-Adaptive Software Systems,” DARPA, 1999 \$98,308 (D. Martin, PI).
27. Principal Investigator, “Control-Parallel Data-Parallel Computer,” Ohio Board of Regents, 1998–2000, \$34,797. (joint award with Kent State University, total award \$79,594).
28. Principal Investigator, “Technology & Software for Semi-Automated, High-Fidelity Validation of VHDL-Related Tools,” Air Force (Wright Laboratory), 1996–99, \$301,020. (joint award with FTL Systems, Inc., total award \$634,830).
29. Co-Principal Investigator, “A Verification Environment for High Assurance Network Performance Prediction,” DARPA, 1996–2000, \$929,009, with P. Alexander.

30. Principal Investigator, “Standard Analyzer of VHDL Applications for Next-Generation Technology (SAVANT),” Air Force (Wright Laboratory), 1995–1998, \$436,351. (an SBIR with extensions sub-contracted to UC from MTL Systems, Inc., total award \$1.4M).
31. Principal Investigator, “Data Paths for MIMD Interpretation,” UC Research Challenge, 1994–1995, \$37,099.
32. Principal Investigator, “A Formal Model of Digital Systems Compatible with VHDL,” ARPA, 1993–1997, \$361,948.
33. Co-Investigator, “QUEST II: Integrated Simulation, Synthesis and ATPG of Large Scale VHDL Descriptions,” ARPA, 1993–1998, \$2,700,000. (H. Carter, PI).
34. Co-Principal Investigator, “Distributed Simulation of VHDL,” MTL Systems, Inc, Dayton, OH (sub-contracting an SBIR from the Air-Force), 1993–1995, \$250,000, with D. Hensgen and H. Carter.
35. Co-Investigator, “Ultra-High-Speed Acceleration of Digital Systems Simulation, Synthesis, and Test Vector Generation,” DARPA, 1989–1993, \$2,600,000. (H. Carter, PI).
36. Co-Principal Investigator, “Simulation of VHDL Design and Prototype Software on Multiple Silicon Graphics Workstations,” Air-Force, 1991–1992, \$74,603, with D. Hensgen.
37. Principal Investigator, “Computational Aspects of Design Verification,” SUN Microsystems Inc., 1991–1993, \$107,650. (equipment grant).
38. Principal Investigator, “Ada Compiler Evaluation: A Quantitative Analysis,” University Research Council, University of Cincinnati, 1990-91, \$8,553.
39. Principal Investigator, “Ada Compiler Evaluation for Embedded Controller Applications,” General Electric Corporation, 1989–1990, \$44,137
40. Principal Investigator, “Reasoning About Computer Architectures with Interval Temporal Logic,” Battelle, 1989, \$18,000. (equipment grant).
41. Principal Investigator, “Tools and Techniques for Automating the Computer System Design Process,” University Research Council, University of Cincinnati, 1988–1989, \$2,300.
42. Principal Investigator, “Program Verification with Temporal Logic,” Herman Schneider Laboratory, University of Cincinnati, 1987-1988, \$1,850.