# Slides for Your Lab2

In addition to the final report, you have to e-mail Prof. Jone and the TA a pdf file which contains 15 pages. The purpose of these slides is to help myself and the TA (Arun Janart) to quickly understand your design. In these 15 slides, the following messages must be conveyed:

1. The major function of your chip. The high-level circuit architecture must be given such that the chip function can be easily understood.

2. The testing methodology you have adopted, the test-vector generation strategy, number of type of faults covered, expected and actual responses from the chip, and the eventual diagnosis.

3. If you do not have netlist for test pattern generation, you must have your function test patterns as complete as possible. Your functional must toggle as many circuits as possible. If necessary, you should write a small program to generate the best functional test patterns.

You have to present your chip design and test methods in my office for 20 minutes using the pdf you submit, before giving the demonstration in the lab. I have a wide-screen monitor for you to present.